

SPECIFICATION

Device Name : Power MOSFET


Type Name : 2SK3601-01

Spec. No. : **MS5F5112**


Date : Jan.-11- 2002

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Fuji Electric Co.,Ltd.
Matsumoto Factory

| | DATE | NAME | APPROVED | Fuji Electric Co.,Ltd. | | |
|---------|-------------|---------------------|---|------------------------|-----------------|--------|
| DRAWN | Jan.-11-'02 | <i>H. Tokunishi</i> |  | DWG. NO. | MS5F5112 | 1 / 21 |
| CHECKED | Jan.-11-'02 | <i>T. HOSEN</i> | | | | |
| CHECKED | Jan.-11-'02 | <i>M. Wabusa</i> | | | | |

Revised Records

| Date | Classification | Index | Content | Drawn | Checked | Checked | Approved |
|-----------------|----------------|-------|---------|-----------------|----------|-----------|---|
| Jan.-11 2002 | enactment | — | ————— | H. Tokunishi | T. HOSEN | M. Wakasa |  |
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DWG. NO.

MS5F5112

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- 1.Scope** This specifies Fuji Power MOSFET 2SK3601-01
- 2.Construction** N-Channel enhancement mode power MOSFET
- 3.Applications** for Switching
- 4.Outview** TFP Package Outview See to 12/21 page

5.Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

| Description | Symbol | Characteristics | Unit | Remarks |
|----------------------------------|----------------------|-----------------|-------|---------------------------------|
| Drain-Source Voltage | V _{DS} | 100 | V | |
| | V _{D SX} | 70 | V | V _{GS} =-30V |
| Continuous Drain Current | I _D | ± 20 | A | |
| | | ± 4.4 | A | *1 , Ta=25°C |
| Pulsed Drain Current | I _{DP} | ± 80 | A | |
| Gate-Source Voltage | V _{GS} | ± 30 | V | |
| Non-repetitive Avalanche Current | I _{AS} | 20 | A | |
| Maximum Avalanche Energy | E _{AS} | 227 | mJ | L=681μH V _{CC} =48V |
| Maximum Drain-Source dV/dt | dV _{DS} /dt | 20 | kV/μs | V _{DS} ≤100V |
| Peak Diode Recovery dV/dt | dV/dt | 5 | kV/μs | *2 |
| Maximum Power Dissipation | P _D | 50 | W | |
| | | 2.4 | W | *1 , Ta=25°C |
| Operating and Storage | T _{ch} | 150 | °C | |
| Temperature range | T _{stg} | -55 to +150 | °C | |

*1 Surface mounted on 1000mm², t=1.6mm FR-4 PCB (Drain pad area : 500mm²)

*2 I_F≤-I_D, -di/dt=50A/μs, V_{CC}≤BV_{DSS}, T_{ch}≤150°C

6.Electrical Characteristics at Tc=25°C (unless otherwise specified)

Static Ratings

| Description | Symbol | Conditions | min. | typ. | max. | Unit |
|----------------------------------|---------------------|---|------|------|------|------|
| Drain-Source Breakdown Voltage | BV _{DSS} | I _D =250μA V _{GS} =0V | 100 | - | - | V |
| Gate Threshold Voltage | V _{GS(th)} | I _D =250μA V _{DS} =V _{GS} | 3.0 | - | 5.0 | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =100V V _{GS} =0V T _{ch} =25°C | - | - | 25 | μA |
| | | V _{DS} =80V V _{GS} =0V T _{ch} =125°C | - | - | 250 | |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} = ± 30V V _{DS} =0V | | 10 | 100 | nA |
| Drain-Source On-State Resistance | R _{DS(on)} | I _D =10A V _{GS} =10V | - | 47 | 62 | mΩ |

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Dynamic Ratings

| Description | Symbol | Conditions | min. | typ. | max. | Unit |
|------------------------------|--------------|-----------------------------|------|------|------|------|
| Forward Transconductance | g_{fs} | $I_D=10A$ $V_{DS}=25V$ | 6 | 12 | - | S |
| Input Capacitance | C_{iss} | $V_{DS}=75V$ $V_{GS}=0V$ | - | 730 | 1095 | pF |
| Output Capacitance | C_{oss} | | | | | |
| Reverse Transfer Capacitance | C_{rss} | $f=1MHz$ | - | 12 | 18 | pF |
| Turn-On Time | $t_{d(on)}$ | $V_{cc}=48V$ | - | 12 | 18 | ns |
| | t_r | $V_{GS}=10V$ | - | 3.8 | 6 | |
| Turn-Off Time | $t_{d(off)}$ | $I_D=10A$ | - | 23 | 35 | |
| | t_f | $R_{GS}=10\Omega$ | - | 8.5 | 13 | |
| Total Gate Charge | Q_G | $V_{cc}=50V$ | - | 22 | 33 | nC |
| Gate-Source Charge | Q_{GS} | $I_D=20A$ | - | 9 | 13.5 | |
| Gate-Drain Charge | Q_{GD} | $V_{GS}=10V$ | - | 6 | 9 | |

Reverse Diode

| Description | Symbol | Conditions | min. | typ. | max. | Unit |
|--------------------------|----------|---|------|------|------|---------|
| Avalanche Capability | I_{AV} | $L=100\mu H$ $T_{ch}=25^\circ C$ See Fig.1 and Fig.2 | 20 | - | - | A |
| Diode Forward On-Voltage | V_{SD} | $I_F=20A$ $V_{GS}=0V$ $T_{ch}=25^\circ C$ | - | 1.10 | 1.65 | V |
| Reverse Recovery Time | t_{rr} | $I_F=20A$ $V_{GS}=0V$ | - | 65 | - | ns |
| Reverse Recovery Charge | Q_{rr} | $-di/dt=100A/\mu s$ $T_{ch}=25^\circ C$ | - | 0.17 | - | μC |

7.Thermal Resistance

| Description | Symbol | min. | typ. | max. | Unit |
|--------------------|-------------------|------|------|------|--------------|
| Channel to Case | $R_{th(ch-c)}$ | | | 2.50 | $^\circ C/W$ |
| Channel to Ambient | $R_{th(ch-a)}$ | | | 87.0 | $^\circ C/W$ |
| Channel to Ambient | $R_{th(ch-a)} *1$ | | | 52.0 | $^\circ C/W$ |

*1 Surface mounted on $1000mm^2$, $t=1.6mm$ FR-4 PCB (Drain pad area : $500mm^2$)

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Fig.1 Test circuit

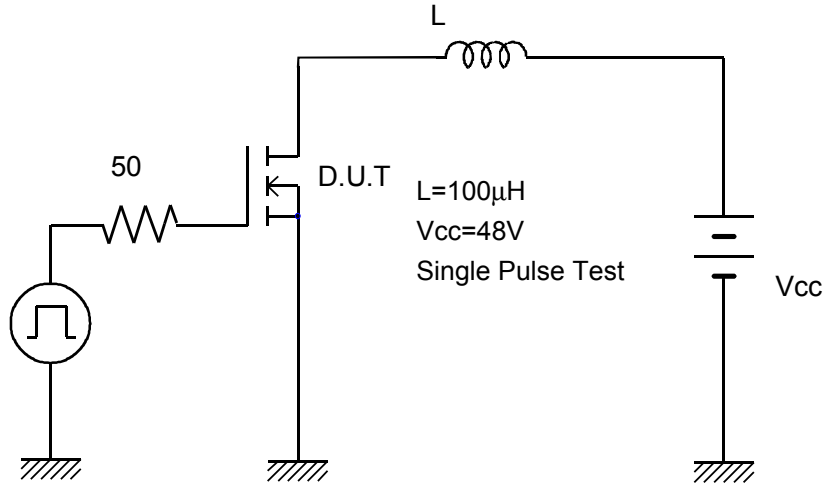
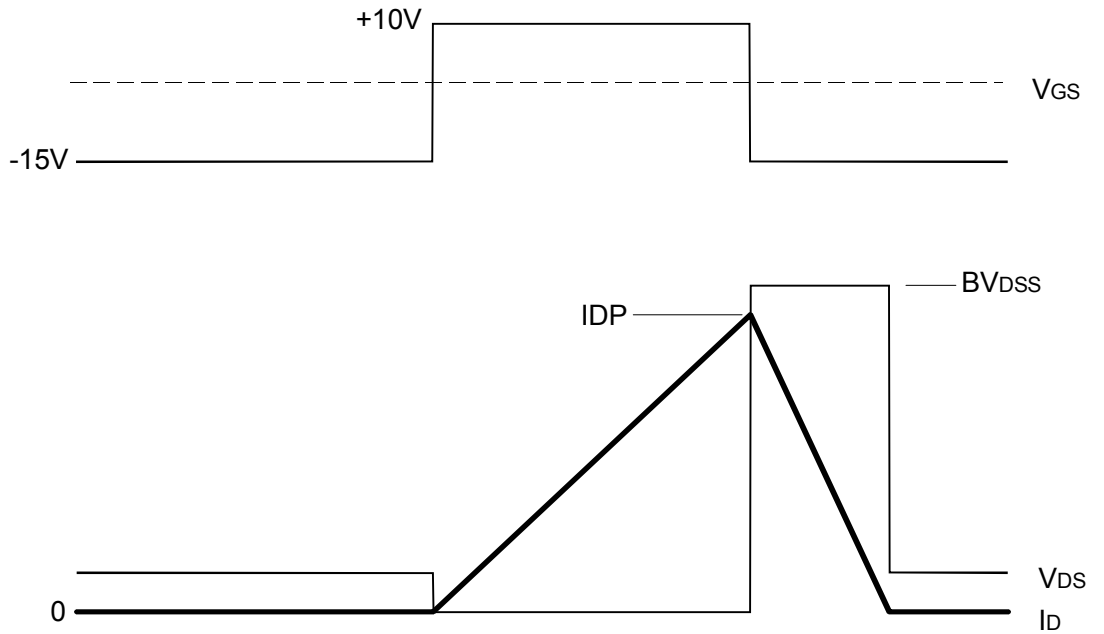


Fig.2 Operating waveforms



8. Reliability test items

All guaranteed values are under the categories of reliability per non-assembled(only MOSFETs).
 Each categories under the guaranteed reliability conform to EIAJ ED4701 B101A standards.

(Test items required without fail : Test Method B-121,B-122,B-123,B-131,B-141
 Humidification treatment (85±2°C,65±5%RH,168±24hr)
 Heat treatment of soldering (IR-ray Reflow ,235±5°C(240°Cmax.),10±1sec,2 times))

| | Test No. | Test Items | Testing methods and Conditions | Reference Standard EIAJ ED4701 | Sampling number | Acceptance number |
|-------------------------|----------|------------------------------|--|-----------------------------------|-----------------|-------------------|
| Mechanical test methods | 1 | Vibration | frequency : 100Hz to 2kHz Acceleration : 100m/s ² Sweeping time : 20min./1 cycle 6times for each X,Y&Z directions. | A-121 test code C | 15 | (0:1) |
| | 2 | Shock | Peak amplitude: 15km/s ² Duration time : 0.5ms 3times for each X,Y&Z directions. | A-122 test code D | 15 | |
| | 3 | Solderability | Solder temp. : 215±5°C Immersion time : 10±0.5sec | A-131A test code B | 15 | |
| | 4 | Resistance to Soldering Heat | Solder temp. : 235±5°C Immersion time : 10±1sec IR-ray Reflowing | A-133A test code 1-A | 15 | |

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| | Test No. | Test Items | Testing methods and Conditions | Reference Standard EIAJ ED4701 | Sampling number | Acceptance number |
|-----------------------|----------|-------------------------------|---|-----------------------------------|-----------------|-------------------|
| Climatic test methods | 1 | High Temp. Storage | Temperature : 150+0/-5°C Test duration : 1000hr | B-111A | 22 | (0:1) |
| | 2 | Low Temp. Storage | Temperature : -55+5/-0°C Test duration : 1000hr | B-112A | 22 | |
| | 3 | Temperature Humidity Storage | Temperature : 85±2°C Relative humidity : 85±5% Test duration : 1000hr | B-121A test code C | 22 | |
| | 4 | Temperature Humidity BIAS | Temperature : 85±2°C Relative humidity : 85±5% Bias Voltage : $V_{DS(max)} * 0.8$ Test duration : 1000hr | B-122A test code C | 22 | |
| | 5 | Unsaturated Pressurized Vapor | Temperature : 130±2°C Relative humidity : 85±5% Vapor pressure : 230kPa Test duration : 96hr | B-123A test code C | 22 | |
| | 6 | Temperature Cycle | High temp.side : 150±5°C Low temp.side : -55±5°C Duration time : HT 30min,LT 30min Number of cycles : 100cycles | B-131A test code A | 22 | |
| | 7 | Thermal Shock | Fluid : pure water(running water) High temp.side : 100+0/-5°C Low temp.side : 0+5/-0°C Duration time : HT 5min,LT 5min Number of cycles : 100cycles | B-141A test code A | 22 | |
| Test for FET | 1 | Intermittent Operating Life | $T_a=25\pm5^\circ\text{C}$ $\Delta T_c=90\text{degree}$ $T_{ch}\leq T_{ch(max.)}$ Test duration : 3000 cycle | D-322 | 22 | (0:1) |
| | 2 | HTRB (Gate-source) | Temperature : 150+0/-5°C Bias Voltage : $V_{GS(max)}$ Test duration : 1000hr | D-323 | 22 | |
| | 3 | HTRB (Drain-Source) | Temperature : 150+0/-5°C Bias Voltage : $V_{DS(max)}$ Test duration : 1000hr | D-323 | 22 | |

Failure Criteria

| | Item | Symbols | Failure Criteria | | Unit |
|----------------------------|--|---------|-------------------------|-------------|----------|
| | | | Lower Limit | Upper Limit | |
| Electrical Characteristics | Breakdown Voltage | BVDSS | LSL * 0.8 | ----- | V |
| | Zero gate Voltage Drain-Source Current | IDSS | ----- | USL * 2 | A |
| | Gate-Source Leakage Current | IGSS | ----- | USL * 2 | A |
| | Gate Threshold Voltage | VGS(th) | LSL * 0.8 | USL * 1.2 | V |
| | Drain-Source on-state Resistance | RDS(on) | ----- | USL * 1.2 | Ω |
| | Forward Transconductance | gfs | LSL * 0.8 | ----- | S |
| | Diode forward on-Voltage | VSD | ----- | USL * 1.2 | V |
| Outview | Marking | | | | |
| | Soldering and other damages | ----- | With eyes or Microscope | | ----- |

* LSL : Lower Specification Limit

* USL : Upper Specification Limit

* Before any of electrical characteristics measure, all testing related to the humidity have conducted after drying the package surface for more than an hour at 150°C.

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9 Warning

9.1. Although Fuji Electric is enhancing product quality and reliability, a small percentage of semiconductor products may become faulty. When using Fuji Electric semiconductor products in your equipment, you are requested to take adequate safety measures to prevent the equipment from causing a physical injury, fire, or other problem if any of the products become faulty. It is recommended to make your design fail-safe, flame retardant, and free of malfunction.

9.2. The products introduced in this Specification are intended for use in the following electronic and electrical equipment which has normal reliability requirements.

- Computers/OA equipments
- Measurement equipments
- Electrical home appliances
- Communications equipment (Terminal devices)
- Machine tools
- Personal equipments
- AV equipments
- Industrial robots
- etc...

9.3. If you need to use a product in this Specification for equipment requiring higher reliability than normal, such as for the equipment listed below, it is imperative to contact Fuji Electric to obtain prior approval. When using these products for such equipment, take adequate measures such as a backup system to prevent the equipment from malfunctioning even if a Fuji's product incorporated in the equipment becomes faulty.

- Transportation equipment (Automotives, Locomotives and ships etc...)
- Backbone network equipment
- Gas alarm, Leakage gas auto breaker
- Burglar alarm, Fire alarm, Emergency equipments etc...
- Traffic-signal control equipment

9.4. Don't use products in this Specification for the equipment requiring strict reliability such as (without limitation)

- Aerospace equipment
- Medical equipment
- Aeronautic equipment
- Submarine repeater equipment
- nuclear control equipment

10. General Notice

10.1. Preventing ESD Damage

Although the gate oxide of Fuji Power MOSFETs is much higher ruggedness to ESD damage than small-Signal MOSFETs and CMOS ICs, careful handling of any MOS devices are an important consideration.

- 1) When handling MOSFETs, hold them by the case (package) and don't touch the leads and terminals.
- 2) It is recommended that any handling of MOSFETs is done while used electrically conductive floor and tablemats that are grounded.
- 3) Before touching a MOSFETs terminal, discharge any static electricity from your body and clothes by grounding out through a high impedance resistor (about 1MΩ)
- 4) When soldering, in order to protect the MOSFETs from static electricity, ground the soldering iron or soldering bath through a low impedance resistor.

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10.2. Short mode failure / Open mode failure

The MOSFETs may be in the risk of having short mode failure or open mode failure when the applied over voltage, over current or over temperature each specified maximum rating. It is recommended to use the fail-safe equipment or circuit from such possible failures.

10.3. An Electric shock / A Skin burn

You may be in risk for an Electric shock or a Skin burn for directly touching to the leads or package of the MOSFETs while turning on electricity or operating.

10.4. Smoke / Fire

Fuji MOSFETs are made of incombustibility material. However, a failure of the MOSFETs may emit smoke or fire. Also, operating the MOSFETs near any flammable place or material may risk the MOSFETs to emit smoke or fire due to the MOSFETs reach high temperature while operated.

10.5. Corrosion / Erosion

Avoid use or storage of the MOSFETs under the higher humidity, corrosive gases. It will lead the device to corrode and possibly cause the device to fail.

10.6. Radiation field

Don't use of the device under the radiation field since the device is not designed for radiation proofing.

11. Notes for Design

11.1. You must design the MOSFETs to be operated within specified maximum ratings (Voltage, Current, Temperature etc...) which are imperative to prevent possible failure or destruction of the device.

11.2. We recommend to use the protection equipment or safety equipment such as fuse, breaker to prevent the fire or damage in case of unexpected accident may have occurred.

11.3. You must design the MOSFETs within its reliability and lifetime in certain the environment or condition. There is a risk that MOSFETs breakdown earlier than the target lifetime of the your products when MOSFETs was used in the reliability condition excessively. Especially avoid use of the MOSFETs under the higher humidity, corrosive gases.

11.4. We recommend to consider for the temperature rise not only for the Channel but also for the Leads if it designed to large current operation to the MOSFETs.

11.5. We only guarantee the non-repetitive Avalanche capability and not for the continuous, repetitive Avalanche capability which can be assumed as abnormal condition. Please note the device may be destructed from the Avalanche over the specified maximum rating.

12. Note on implementation

12.1. Soldering

Soldering involves temperatures which exceed the device storage temperature rating. To avoid device damage and to ensure reliability, the following guidelines from the quality assurance standard must be observed.

1) Solder temperature and duration (Through-Hole Package)

| Solder temperature | Duration |
|--------------------|-----------------|
| 260±5 °C | 10±1 seconds |
| 350±10 °C | 3.5±0.5 seconds |

2) The device should not be soldered closer than 1mm from the package. (* through-hole package)

3) When flow soldered, care must be taken to avoid immersing the package in the solder-bath.

12.2. Please see to the following the Torque reference when mounting the device to heat sink. Excess torque applied to the mounting screw causes damage to the device and weak torque will increase the thermal resistance. Both of these conditions may lead the device to be destructed.

Table 1 : Recommended tightening torques.

| Package style | Screw | Recommended tightening torques |
|---------------------------|-------|--------------------------------|
| TO-220 TO-220F | M3 | 30 – 50 Ncm |
| TO-3P TO-3PF TO-247 | M3 | 40 – 60 Ncm |
| TO-3PL | M3 | 60 –80 Ncm |

12.3. If the heat sink with coarse finish is used, increase in thermal resistance and concentrated force to a point may cause the MOSFETs to be destructed. We recommend in such condition to process the surface of heat sink within $\pm 50\mu\text{m}$ and use of thermal compound to optimize its efficiency of heat radiation. Moreover, it is important to evenly apply the compound and eliminate any air voids. A simple method is to apply a dot of compound of the appropriate quantity to the center of the case just below the chip mount.

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13. Notes for Storage

- 13.1. The MOSFETs should be stored at a standard temperature of 5 to 35 °C and humidity of 45 to 75%RH. If the storage area is very dry, a humidifier may be required. In such a case, use only deionized water or boiled water, since the chlorine in tap water may corrode the leads.
- 13.2. Avoid exposure to corrosive gases and dust.
- 13.3. Rapid temperature changes may cause condensation on the MOSFETs surface. Therefore, store the MOSFETs in a place with few temperature changes.
- 13.4. While in storage, it is important that nothing be loaded on top of the MOSFETs, since this may cause excessive external force on the case.
- 13.5. Store MOSFETs with unprocessed lead terminals. Rust may cause presoldered connections to go bad during later processing.
- 13.6. Use only antistatic containers or shipping bag for storing MOSFETs.

14. Additional points

If you have any question about any portion in this Specification, ask Fuji Electric or its sales agents before using the product. Neither Fuji nor its agents shall be liable for any injury caused by any use of the products not in accordance with instructions set forth herein.

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FUJI POWER MOS FET

OUT VIEW

外形寸法図

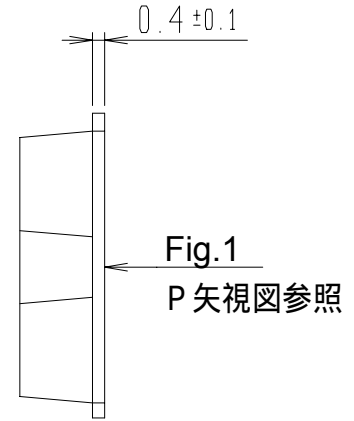
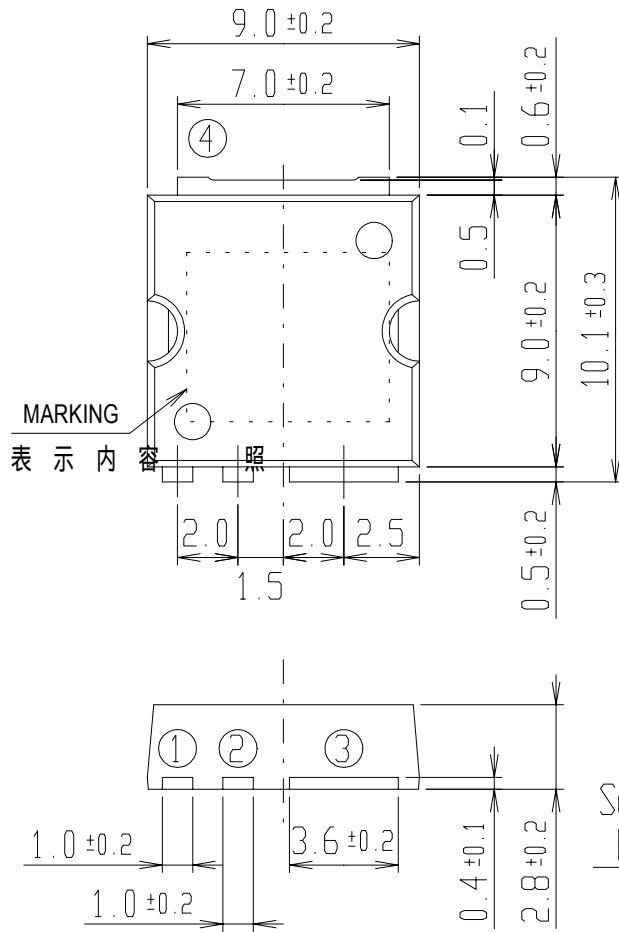
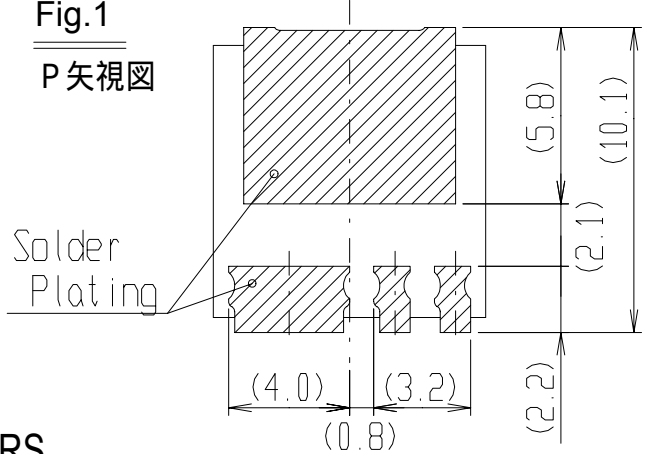


Fig. 1
P 矢視圖



DIMENSIONS ARE IN MILLIMETERS.

Note: 1. Dimension shown in () is reference values.

注) 1. () 内寸法は参考値とする。

MARKING

表示内容

Trademark

商標

Special specification for customer

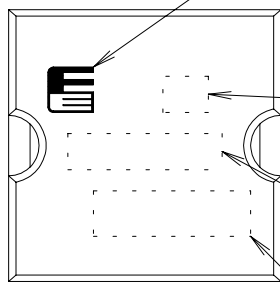
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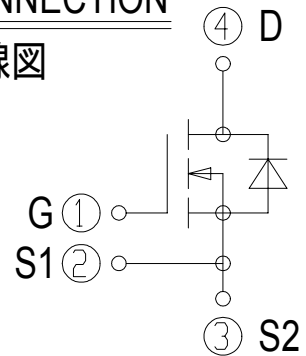
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形名



CONNECTION

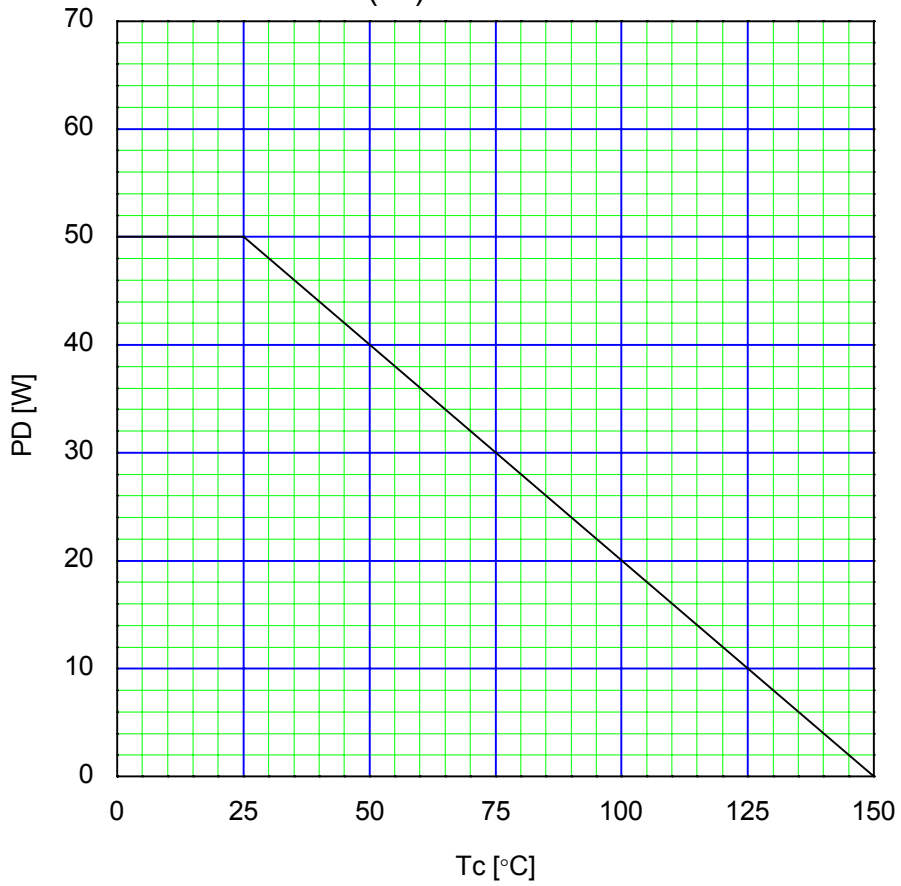
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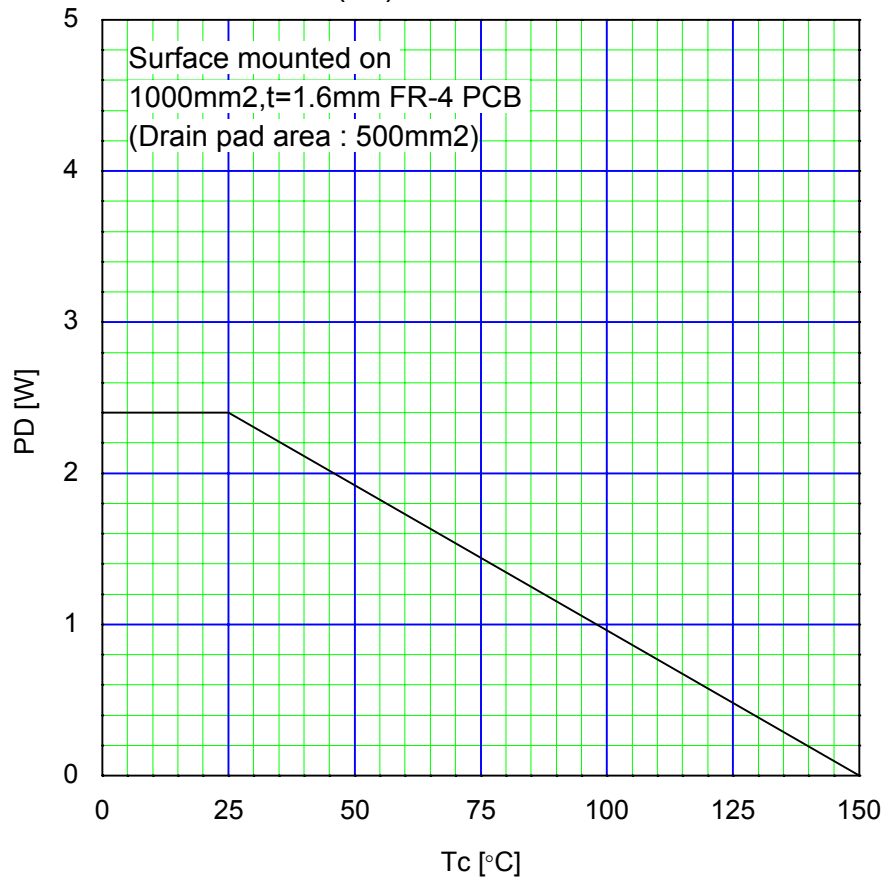
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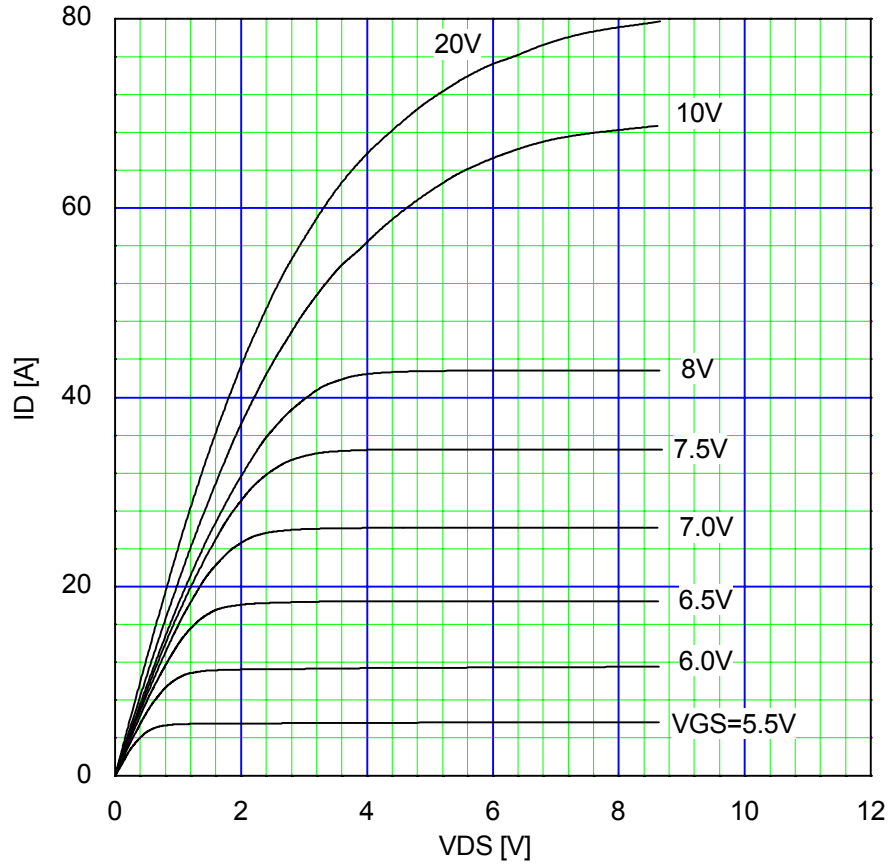
Allowable Power Dissipation $PD=f(T_c)$



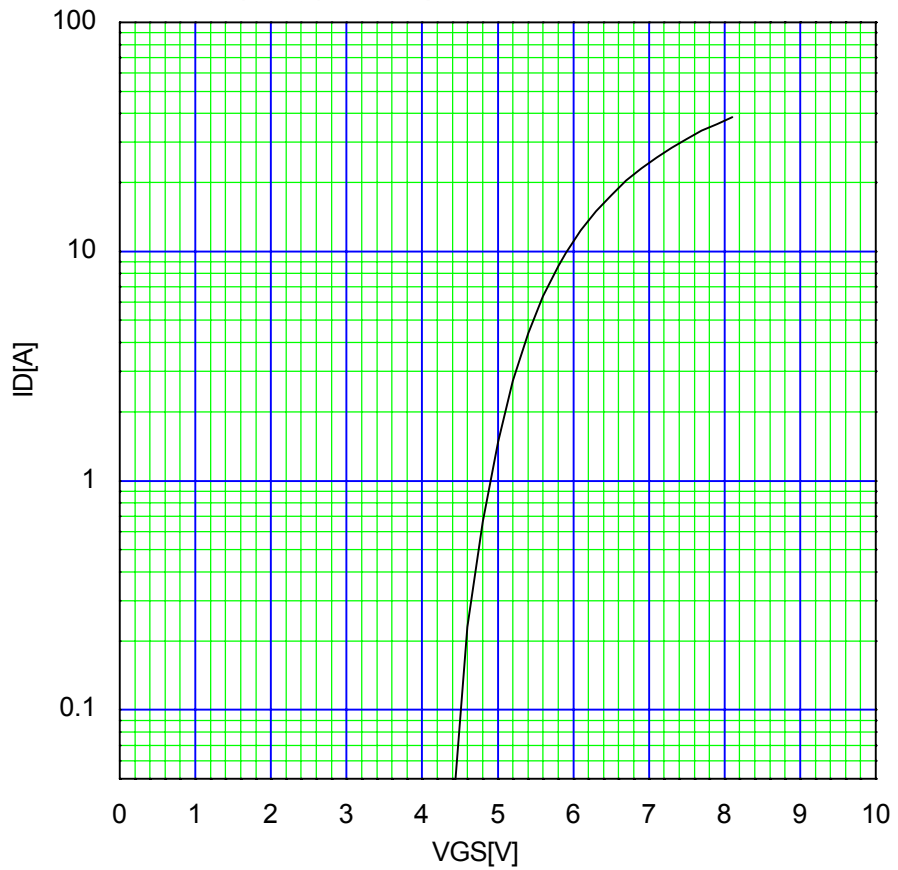
Allowable Power Dissipation $PD=f(T_c)$



Typical Output Characteristics
 $I_D=f(V_{DS}):80\ \mu\text{s}$ pulse test, $T_{ch}=25^\circ\text{C}$



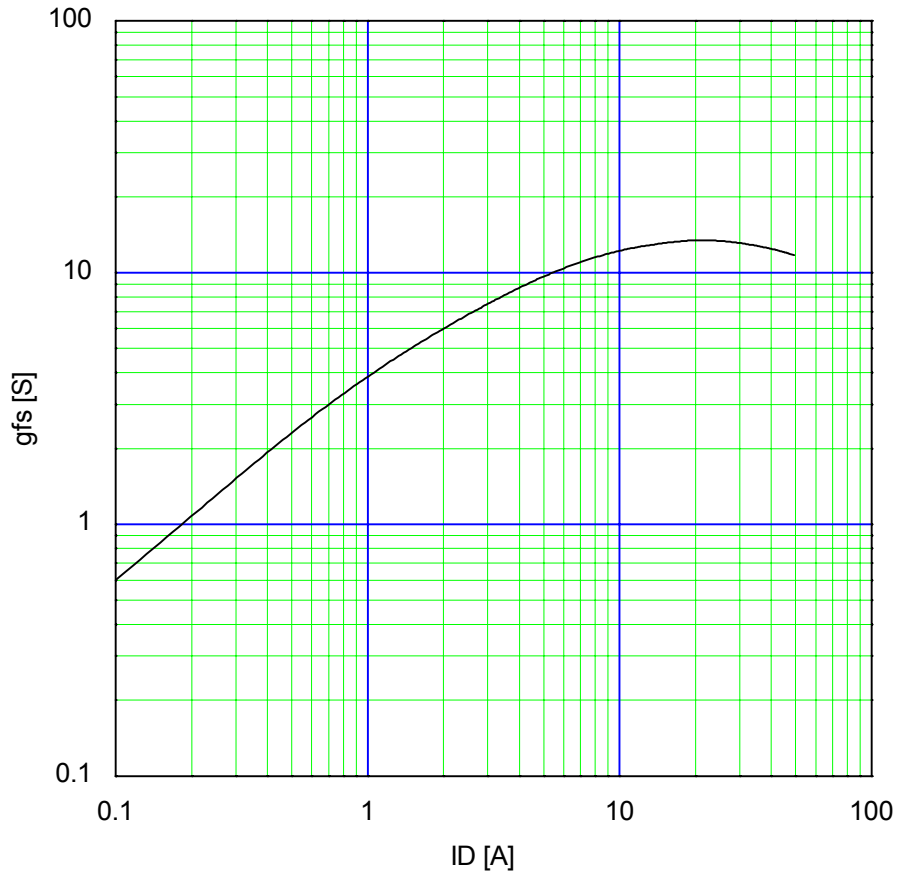
Typical Transfer Characteristic
 $I_D=f(V_{GS}):80\ \mu\text{s}$ pulse test, $V_{DS}=25\text{V}$, $T_{ch}=25^\circ\text{C}$



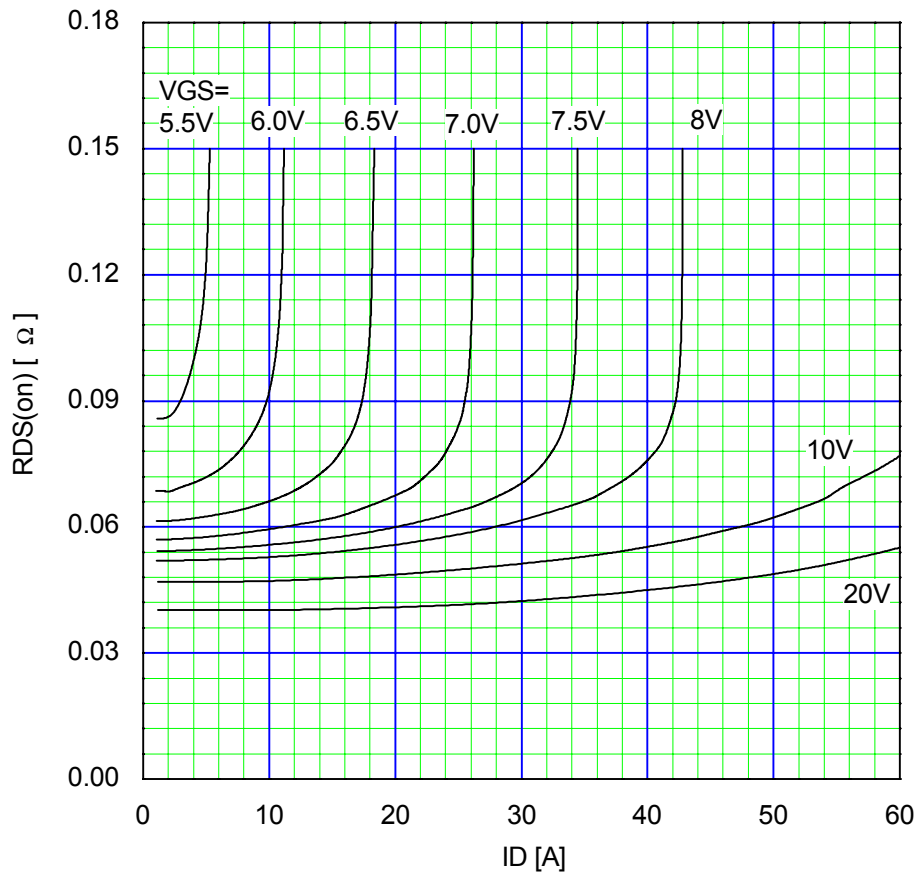
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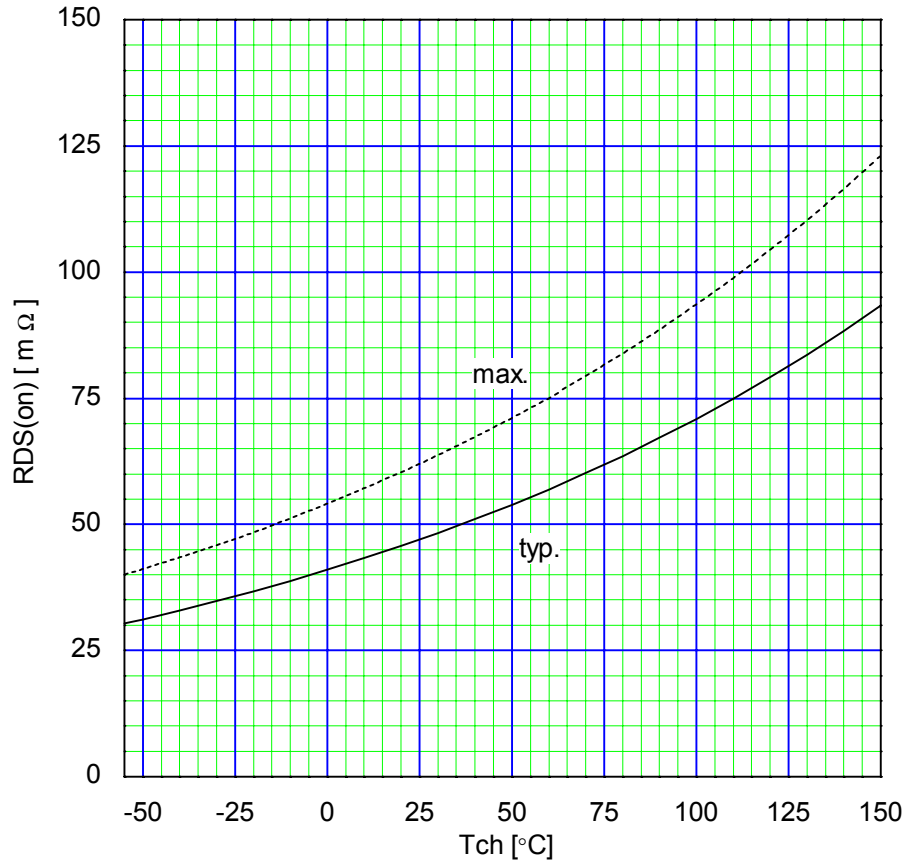
Typical Transconductance
 $g_{fs}=f(I_D):80\ \mu\text{s pulse test}, V_{DS}=25\text{V}, T_{ch}=25^\circ\text{C}$



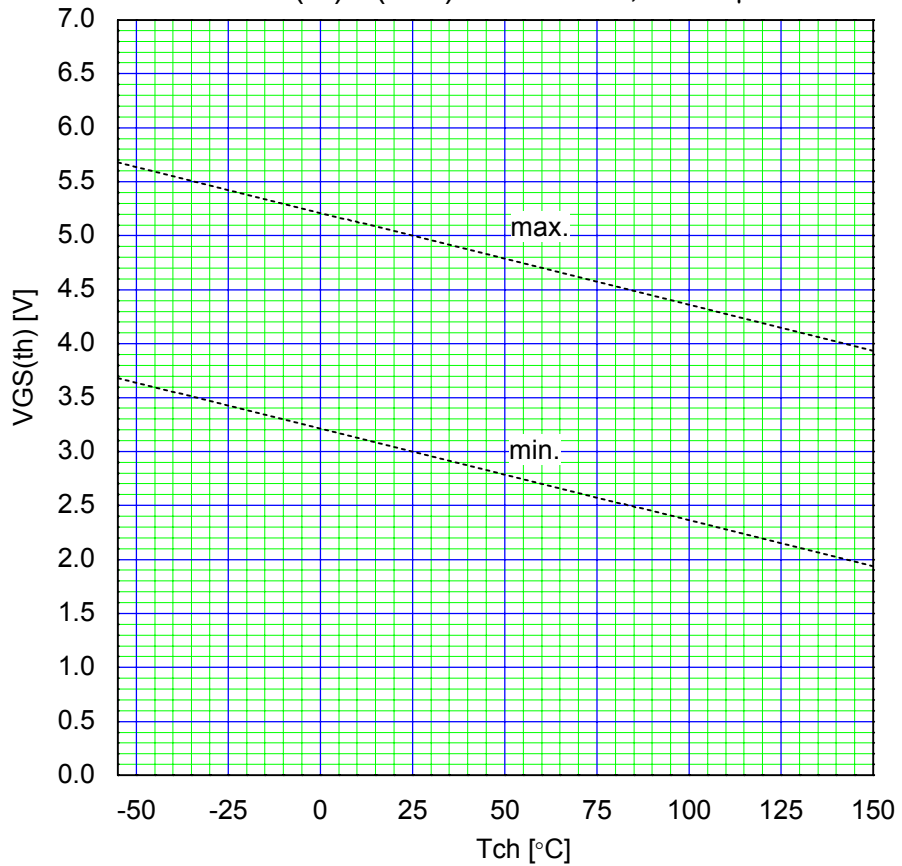
Typical Drain-Source on-state Resistance
 $R_{DS(on)}=f(I_D):80\ \mu\text{s pulse test}, T_{ch}=25^\circ\text{C}$



Drain-Source On-state Resistance
 $R_{DS(on)} = f(T_{ch}): I_D = 10A, V_{GS} = 10V$



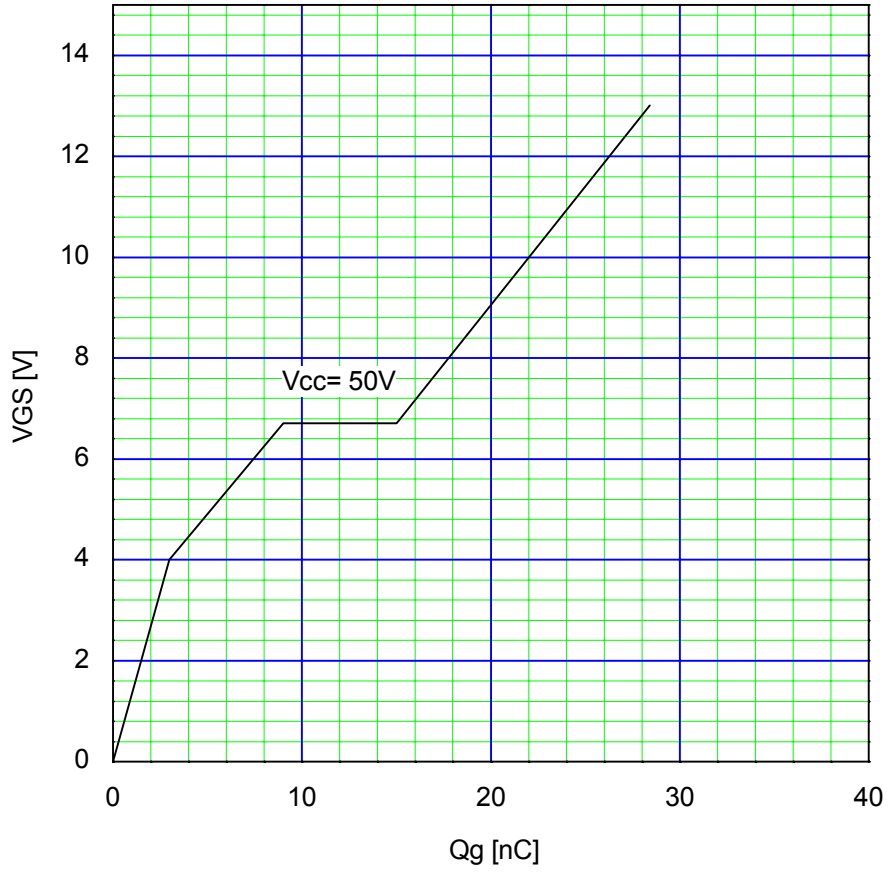
Gate Threshold Voltage vs. T_{ch}
 $V_{GS(th)} = f(T_{ch}): V_{DS} = V_{GS}, I_D = 25\mu A$



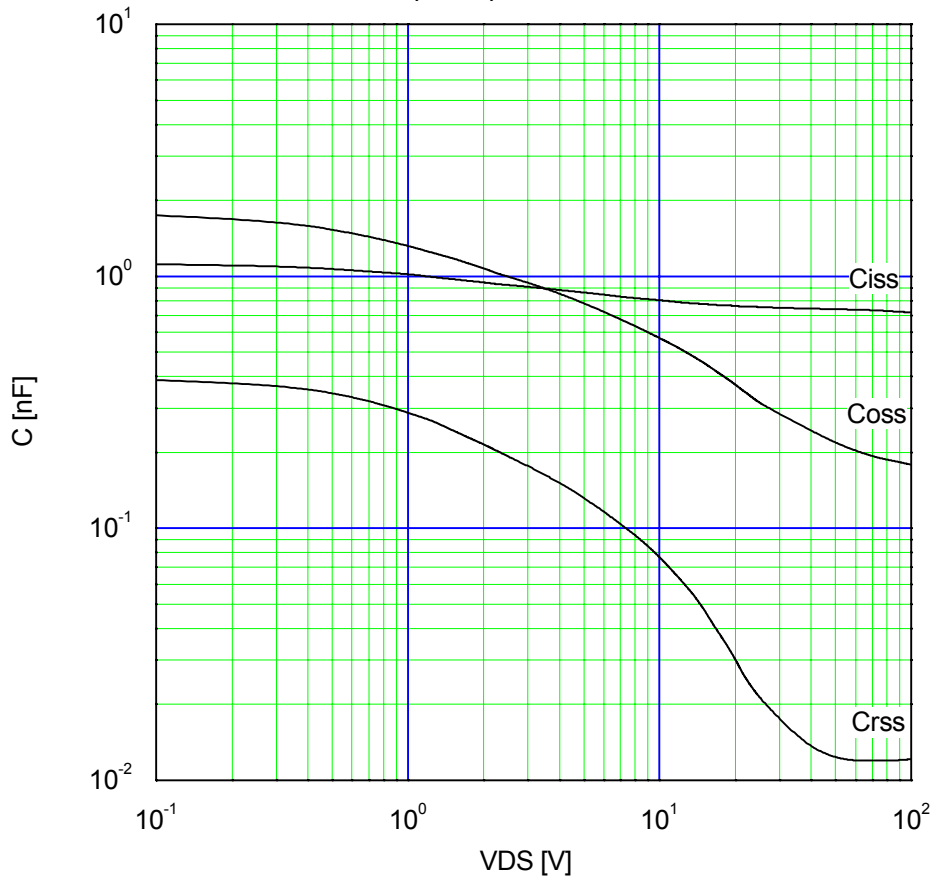
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Typical Gate Charge Characteristics
 $V_{GS}=f(Q_g): I_D=20A, T_{ch}=25^\circ C$



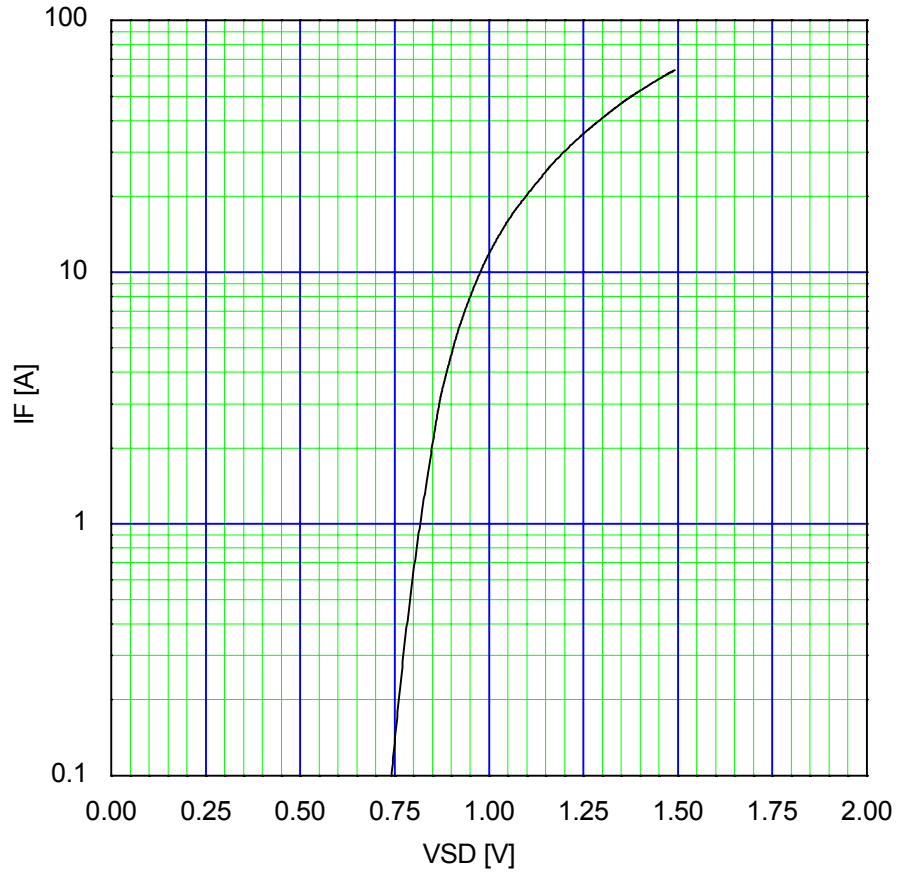
Typical Capacitance
 $C=f(V_{DS}): V_{GS}=0V, f=1MHz$



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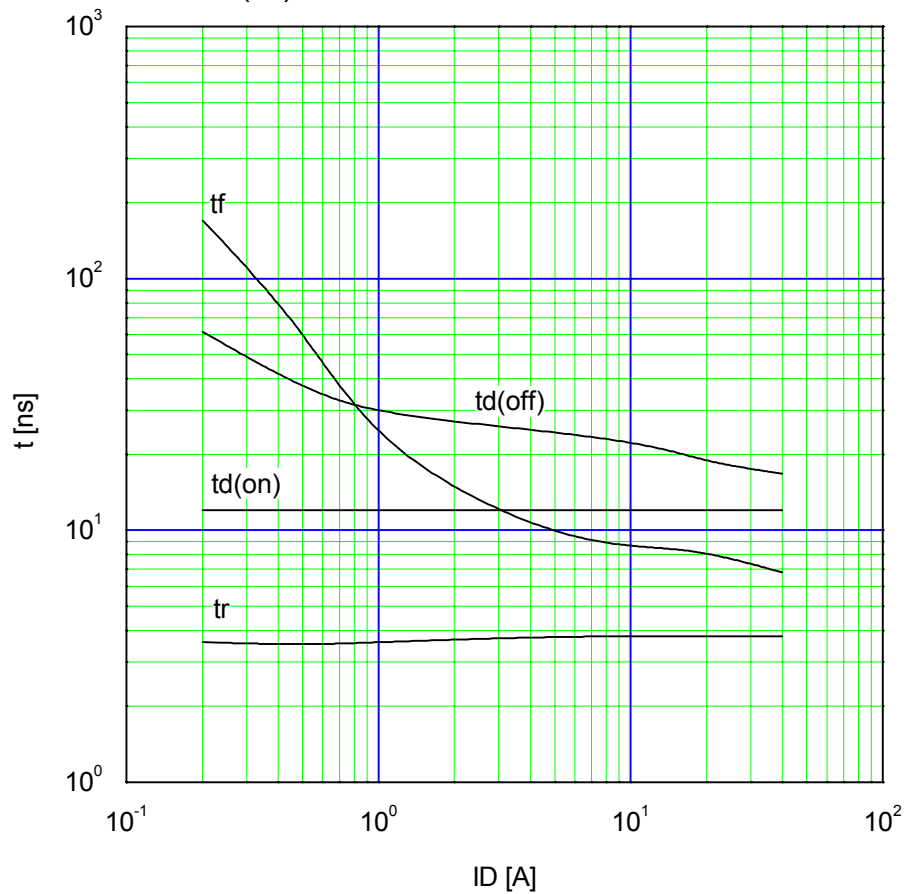
Typical Forward Characteristics of Reverse Diode

$I_F=f(V_{SD}):80\ \mu\text{s pulse test}, T_{ch}=25^\circ\text{C}$



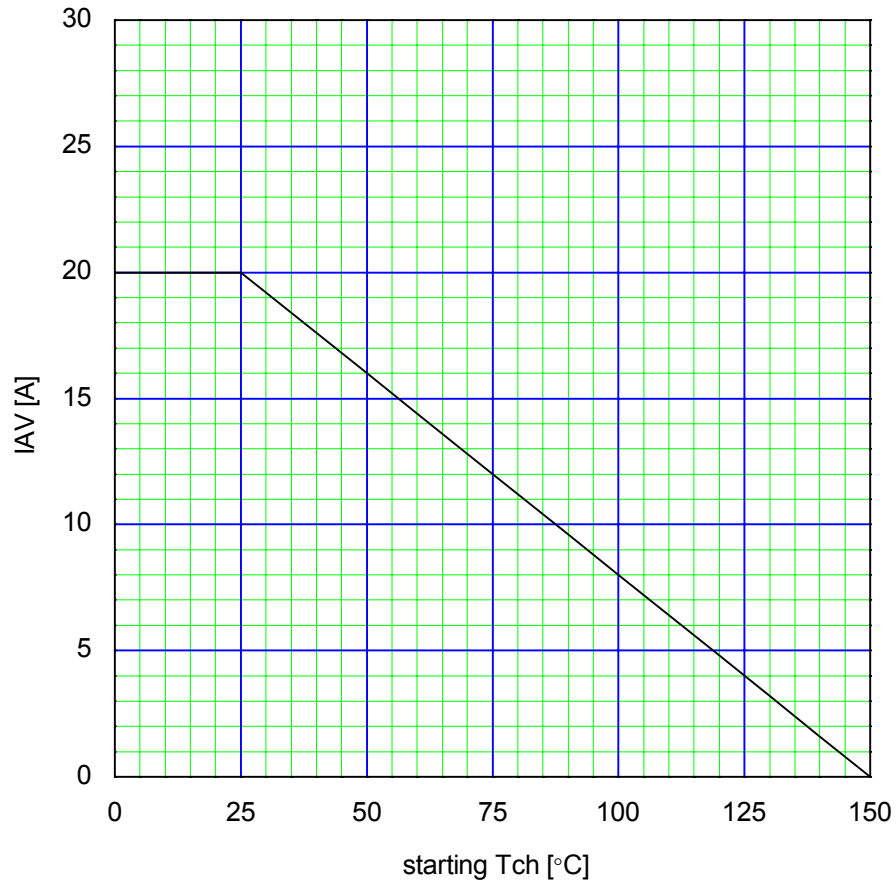
Typical Switching Characteristics vs. ID

$t=f(I_D):V_{cc}=48\text{V}, V_{GS}=10\text{V}, R_G=10\ \Omega$

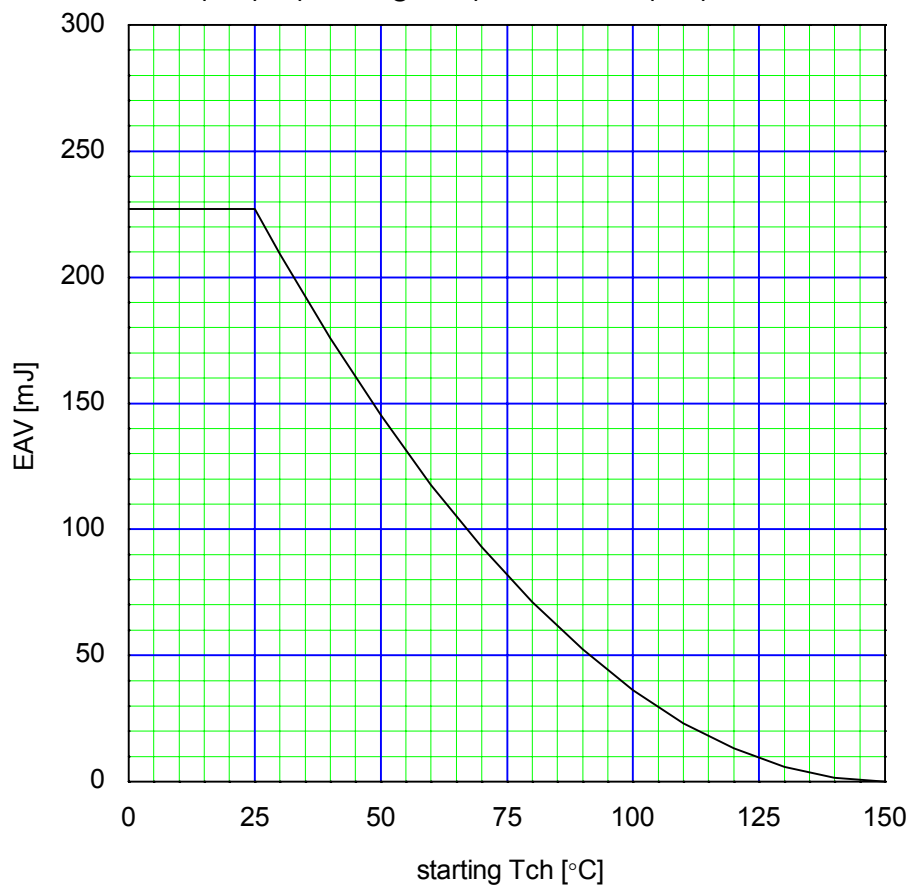


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Maximum Avalanche Current vs. starting Tch
 $I(AV)=f(\text{starting Tch}):V_{CC}=48V$



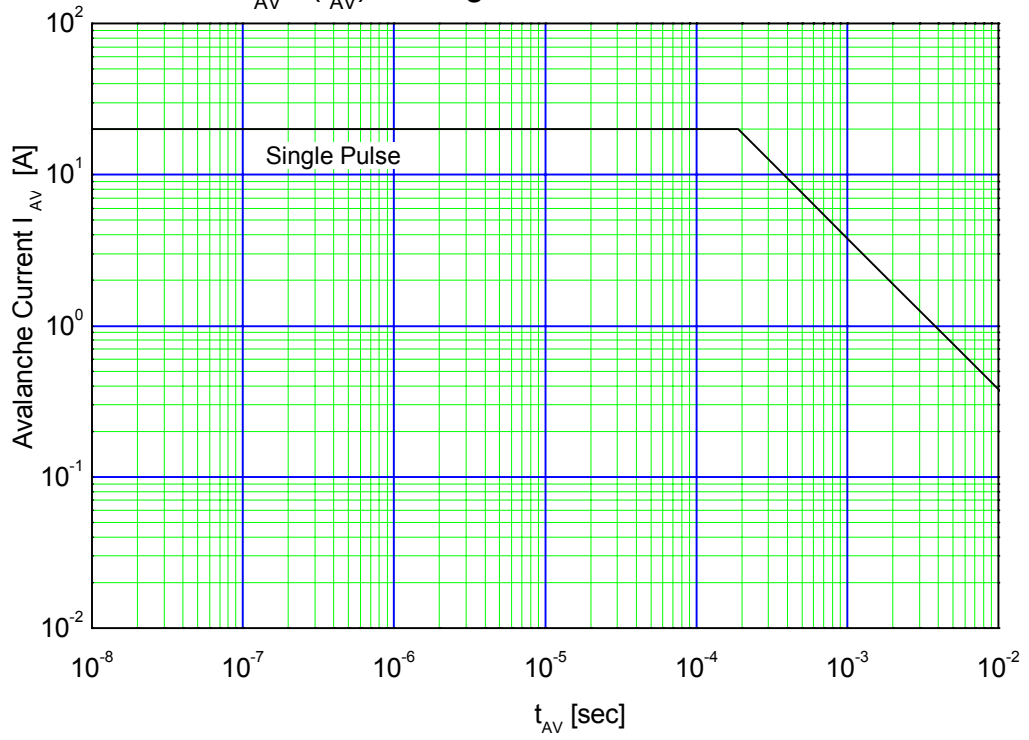
Maximum Avalanche Energy vs. starting Tch
 $E(AV)=f(\text{starting Tch}):V_{CC}=48V, I(AV)\leq 20A$



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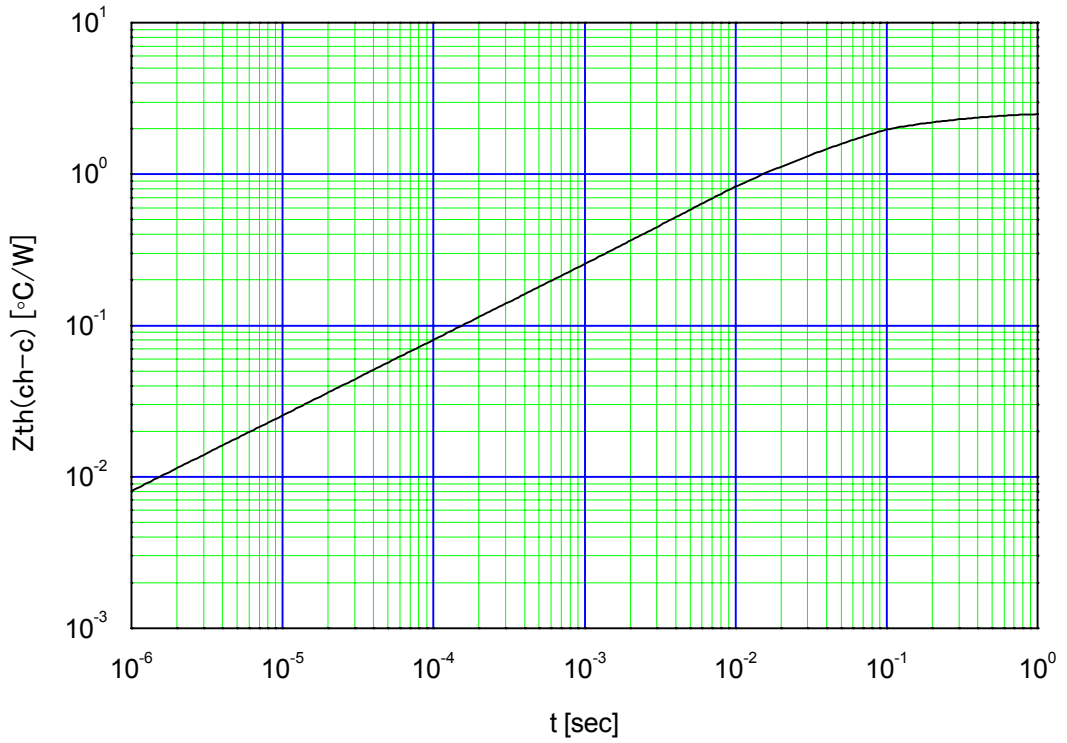
Maximum Avalanche Current Pulsewidth

$$I_{AV} = f(t_{AV}); \text{starting } T_{ch} = 25^{\circ}\text{C}, V_{cc} = 48\text{V}$$



Transient Thermal Impedance

$$Z_{th}(ch-c) = f(t); \text{Duty} = 0$$



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Thermal Resistance vs. Drain Pad area t=1.6mm FR-4 PCB

