

## 2SK3527-01

- 1.Scope** This specifies Fuji Power MOSFET 2SK3527-01
- 2.Construction** N-Channel enhancement mode power MOSFET
- 3.Applications** for Switching
- 4.Outview** TO-247

**5.Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)**

Description	Symbol	Characteristics	Unit	Remarks
Drain-Source Voltage	$V_{DS}$	600	V	
Continuous Drain Current	$I_D$	$\pm 17$	A	
Pulsed Drain Current	$I_{DP}$	$\pm 68$	A	
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V	
Maximum Avalanche Current	$I_{AR}$	17	A	Tch $\leq$ 150°C
Maximum Avalanche Energy	$E_{AV}$	412	mJ	L=2.61mH Vcc=60V
Maximum Drain-Source dV/dt	d $V_{DS}$ /dt	20	kV/ $\mu$ s	VDS $\leq$ 600V
Peak Diode Recovery dV/dt	dV/dt	5	kV/ $\mu$ s	*1
Maximum Power Dissipation	$P_D$	2.5	W	Ta=25°C
		220		Tc=25°C
Operating and Storage	T <sub>ch</sub>	150	°C	
Temperature range	T <sub>stg</sub>	-55 to +150	°C	

\*1  $I_F \leq I_D$ , -di/dt=50A/ $\mu$ s, Vcc $\leq$ BV<sub>DSS</sub>, Tch $\leq$ 150°C

**6.Electrical Characteristics at Tc=25°C (unless otherwise specified)****Static Ratings**

Description	Symbol	Conditions	min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$I_D=250\mu A$ V <sub>GS</sub> =0V	600	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$I_D=250\mu A$ V <sub>DS</sub> =V <sub>GS</sub>	3.0	-	5.0	V
Zero Gate Voltage Drain Current	$I_{DSS}$	V <sub>DS</sub> =600V V <sub>GS</sub> =0V T <sub>ch</sub> =25°C	-	-	25	$\mu A$
		V <sub>DS</sub> =480V V <sub>GS</sub> =0V T <sub>ch</sub> =125°C	-	-	250	
Gate-Source Leakage Current	$I_{GSS}$	V <sub>GS</sub> = $\pm 30$ V V <sub>DS</sub> =0V	-	10	100	nA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	$I_D=8.5A$ V <sub>GS</sub> =10V	-	0.28	0.37	$\Omega$

DATE	NAME	APPROVED	Fuji Electric Co.,Ltd.		
DRAWN '01-07-18	A. Kurosaki		DWG.NO.	MT5F11621	1/3
CHECKED '01-07-18	T. Yamada	T. HOSEN			
REVISIONS					

This material and the information herein is the property of Fuji Electric Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Co.,Ltd.

### Dynamic Ratings

Description	Symbol	Conditions	min.	typ.	max.	Unit
Forward Transconductance	$g_{fs}$	$I_D=8.5A$ $V_{DS}=25V$	(11)		-	S
Input Capacitance	$C_{iss}$	$V_{DS}=25V$	-		(4600)	pF
Output Capacitance	$C_{oss}$	$V_{GS}=0V$	-		(465)	
Reverse Transfer Capacitance	$C_{rss}$	$f=1MHz$	-		(20)	
Turn-On Time	$t_{d(on)}$	$V_{cc}=300V$	-		(50)	ns
	$t_r$	$V_{GS}=10V$	-		(45)	
Turn-Off Time	$t_{d(off)}$	$I_D=8.5A$	-		(90)	
	$t_f$	$R_{GS}=10\Omega$	-		(30)	
Total Gate Charge	$Q_G$	$V_{cc}=300V$	-		(100)	nC
Gate-Source Charge	$Q_{GS}$	$I_D=17A$	-		(40)	
Gate-Drain Charge	$Q_{GD}$	$V_{GS}=10V$	-		(35)	

### Reverse Diode

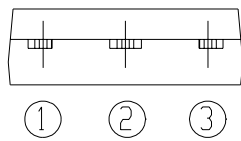
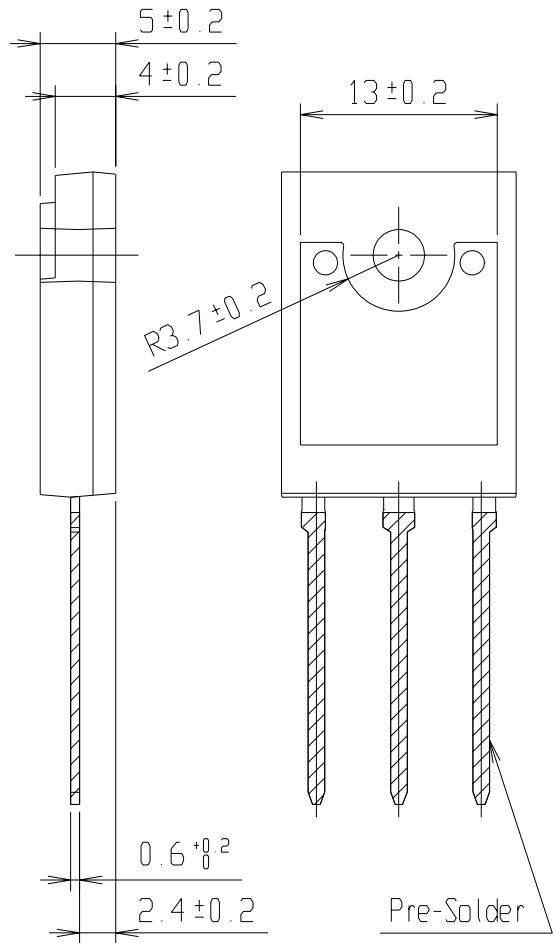
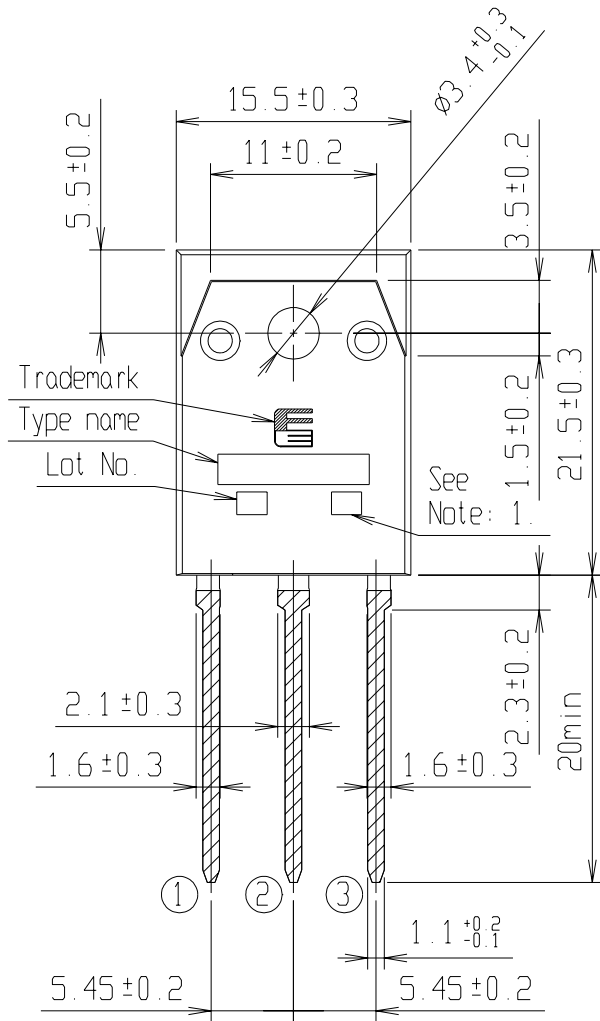
Description	Symbol	Conditions	min.	typ.	max.	Unit
Avalanche Capability	$I_{AV}$	$L=2.61mH$ $T_{ch}=25^\circ C$ $V_{cc}=60V$	17	-	-	A
Diode Forward On-Voltage	$V_{SD}$	$I_F=17A$ $V_{GS}=0V$ $T_{ch}=25^\circ C$	-	1.00	1.50	V
Reverse Recovery Time	$t_{rr}$	$I_F=17A$ $V_{GS}=0V$	-	1.32	-	$\mu s$
Reverse Recovery Charge	$Q_{rr}$	$-di/dt=100A/\mu s$ $T_{ch}=25^\circ C$	-	12.0	-	$\mu C$

### 7. Thermal Resistance

Description	Symbol	min.	typ.	max.	Unit
Channel to Case	$R_{th(ch-c)}$			0.57	$^\circ C/W$
Channel to Ambient	$R_{th(ch-a)}$			50.0	$^\circ C/W$

	DATE	NAME	APPROVED	Fuji Electric Co.,Ltd.	
DRAWN					
CHECKED				DWG. NO.	MT5F11621 2/3
REVISIONS					

This material and the information herein is the property of Fuji Electric Co.,Ltd. They shall be neither reproduced, copied, lent, or disclosed in any way whatsoever for the use of any third party nor used for the manufacturing purposes without the express written consent of Fuji Electric Co.,Ltd.



CONNECTION

- ① GATE
- ② DRAIN
- ③ SOURCE

JEDEC : TO-247

Note: 1. Guaranteed mark of avalanche ruggedness.

DIMENSIONS ARE IN MILLIMETERS.

	DATE	NAME	APPROVED	Fuji Electric Co.,Ltd.	
DRAWN					
CHECKED				DWG.NO.	MT5F11621 3/3
REVISIONS					