

# SPECIFICATION

DEVICE NAME : Power MOSFET

TYPE NAME : 2SK2000-R

SPEC. No. :

Fuji Electric Co.,Ltd.

Fuji Semiconductor, Inc. - P.O. Box 702708 - Dallas, TX 75370 - 972-733-1700 - [www.fujisemiconductor.com](http://www.fujisemiconductor.com)

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	DATE	NAME	APPROVED	Fuji Electric Co.,Ltd.	
DRAWN				DWG.NO.	1/10
CHECKED					

1. Scope  
This specifies Fuji power MOSFET 2SK2000-R
2. Construction N-channel enhancement mode power MOSFET
3. Application for switching
4. Outview TO-3PF Outview See to 4/10 page
5. Absolute maximum ratings at  $T_c=25^\circ\text{C}$  (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain-source voltage	$V_{DS}$	100	V	
Drain-gate voltage	$V_{DGR}$	100	V	$R_{GS} = 20\text{K}\Omega$
Continuous Drain current	$I_D$	$\pm 32$	A	
Pulsed drain current	$I_{Dpulse}$	$\pm 128$	A	
Gate-source voltage	$V_{GS}$	$\pm 20$	V	
Maximum power dissipation	$P_D$	80	W	
Operating and storage temperature range	$T_{ch}$	150	$^\circ\text{C}$	
	$T_{stg}$	-55 ~ +150	$^\circ\text{C}$	

6. Electrical characteristics at  $T_c=25^\circ\text{C}$  (unless otherwise specified)  
Static ratings

Description	Symbol	Conditions	Characteristics			Unit
			Min.	Typ.	Max.	
Drain-source breakdown voltage	$BV_{DSS}$	$I_D = 1\text{mA}$ $V_{GS} = 0\text{V}$	100			V
Gate threshold voltage	$V_{GS(th)}$	$I_D = 10\text{mA}$ $V_{DS} = V_{GS}$	2.1	3.0	4.0	V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 100\text{V}$ $V_{GS} = 0\text{V}$	$T_{ch} = 25^\circ\text{C}$	10	500	$\mu\text{A}$
	$I_{DSS}$		$T_{ch} = 125^\circ\text{C}$	0.2	1.0	mA
Gate-source leakage current	$I_{GSS}$	$V_{GS} = \pm 20\text{V}$ $V_{DS} = 0\text{V}$		10	100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$I_D = 16\text{A}$ $V_{GS} = 10\text{V}$		0.05	0.06	$\Omega$

Dynamic ratings

Description	Symbol	Conditions	Characteristics			Unit
			Min.	Typ.	Max.	
Forward transconductance	$g_{fs}$	$I_D = 16A$ $V_{DS} = 25V$	10	20		S
Input capacitance	$C_{iss}$	$V_{DS} = 25V$ $V_{GS} = 0V$ $f = 1MHz$		2000	3000	pF
Output capacitance	$C_{oss}$			500	800	pF
Reverse transfer capacitance	$C_{rss}$			120	180	pF
Turn-on time	$t_{d(on)}$	$V_{CC} = 30V$ $V_{GS} = 10V$ $I_D = 3A$ $R_{GS} = 50\Omega$		40	60	ns
	$t_r$			80	120	ns
Turn-off time	$t_{d(off)}$			350	500	ns
	$t_f$			120	180	ns

Reverse diode

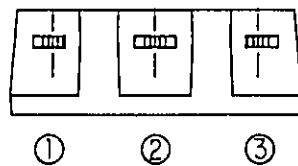
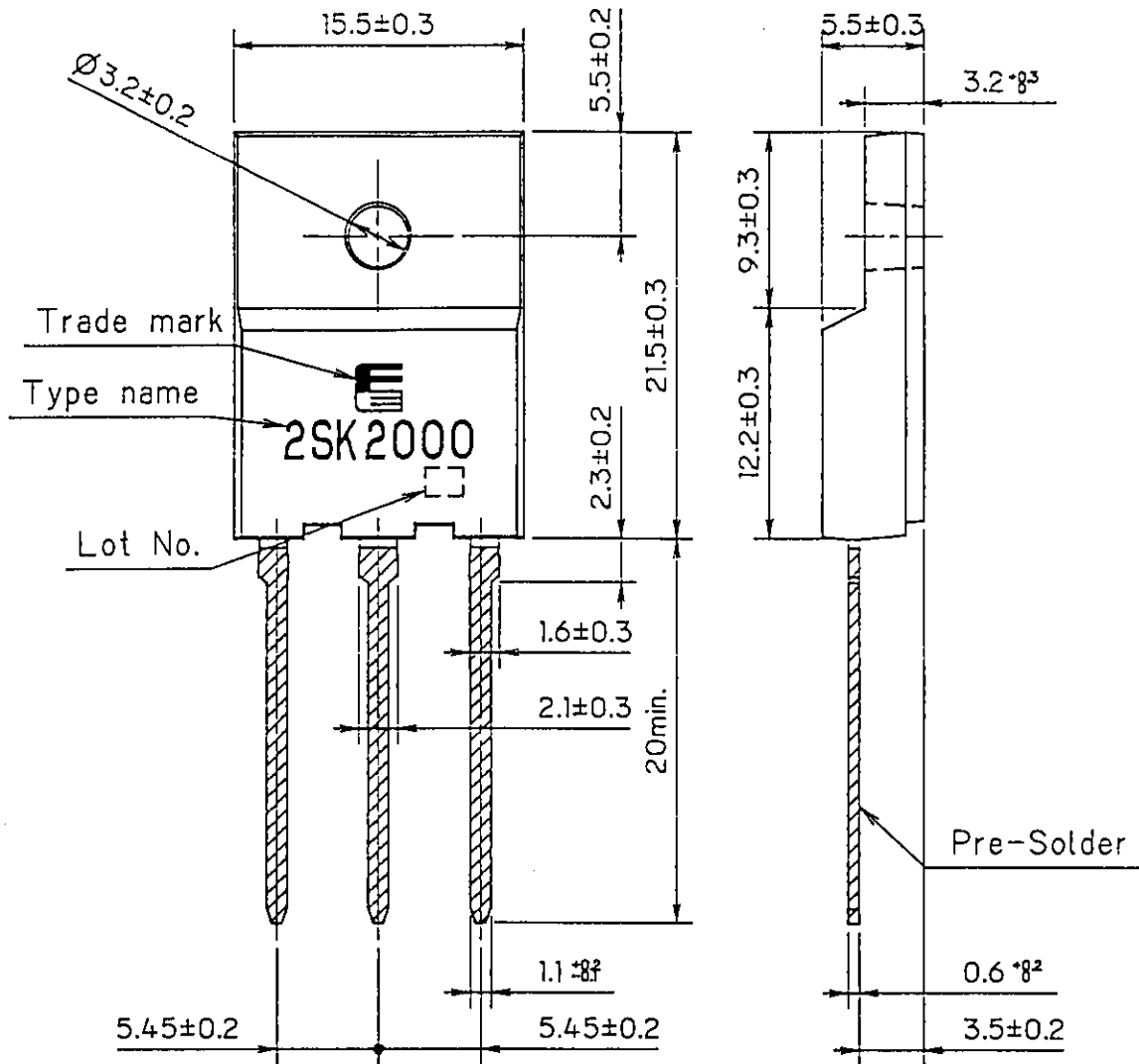
Description	Symbol	Conditions	Characteristics			Unit
			Min.	Typ.	Max.	
Diode forward on-voltage	$V_{SD}$	$I_F = 2 \times I_{DR}$ $V_{GS} = 0V, T_{CH} = 25^\circ C$		1.5	2.0	V
Reverse recovery time	$t_{rr}$	$I_F = I_{DR}$ $V_{GS} = 0V$ $-di_F/dt = 100A/\mu s$ $T_{CH} = 25^\circ C$		150		ns
Reverse recovery charge	$Q_{rr}$			1		$\mu C$

7. Thermal resistance

Description	Symbol	Conditions	Characteristics			Unit
			Min.	Typ.	Max.	
Thermal resistance	$R_{th_{ch-c}}$				1.56	$^\circ C/W$
	$R_{th_{ch-a}}$				30.0	$^\circ C/W$

FUJI POWER MOSFET

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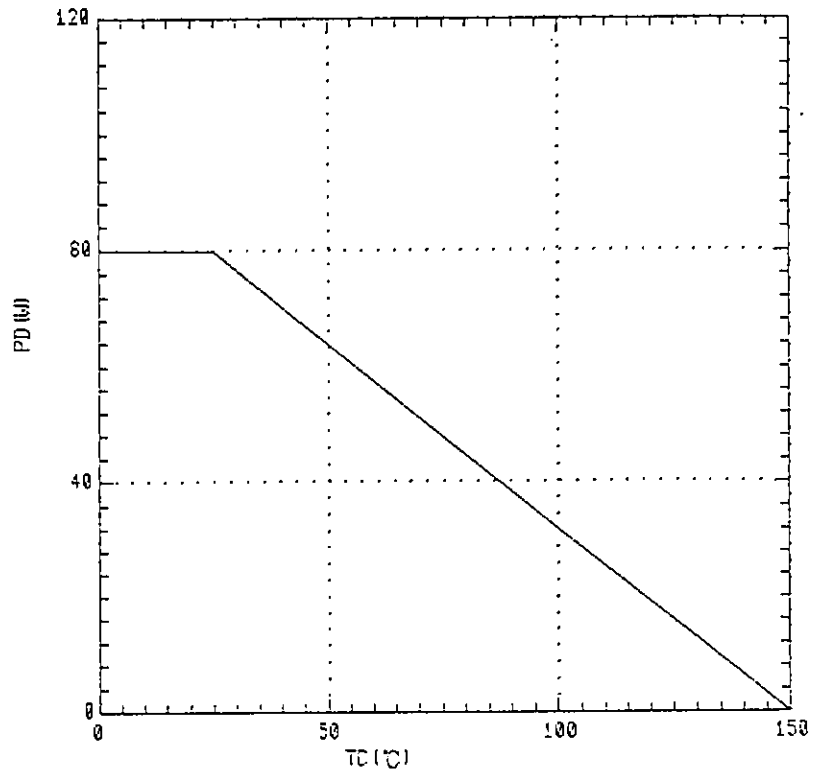


CONNECTION

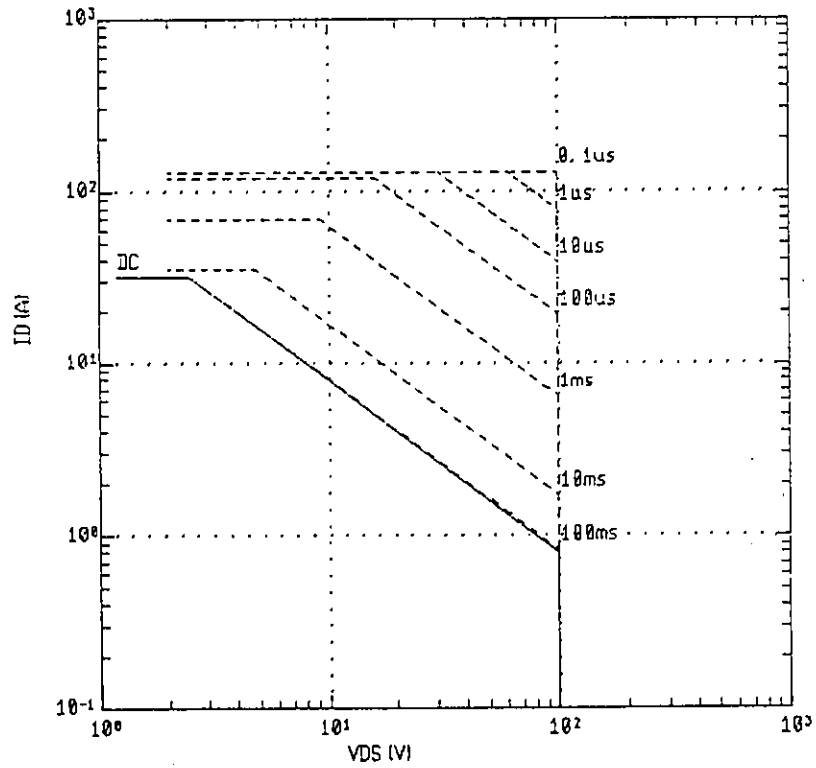
- ① GATE
- ② DRAIN
- ③ SOURCE

DIMENSIONS ARE IN MILLIMETERS.

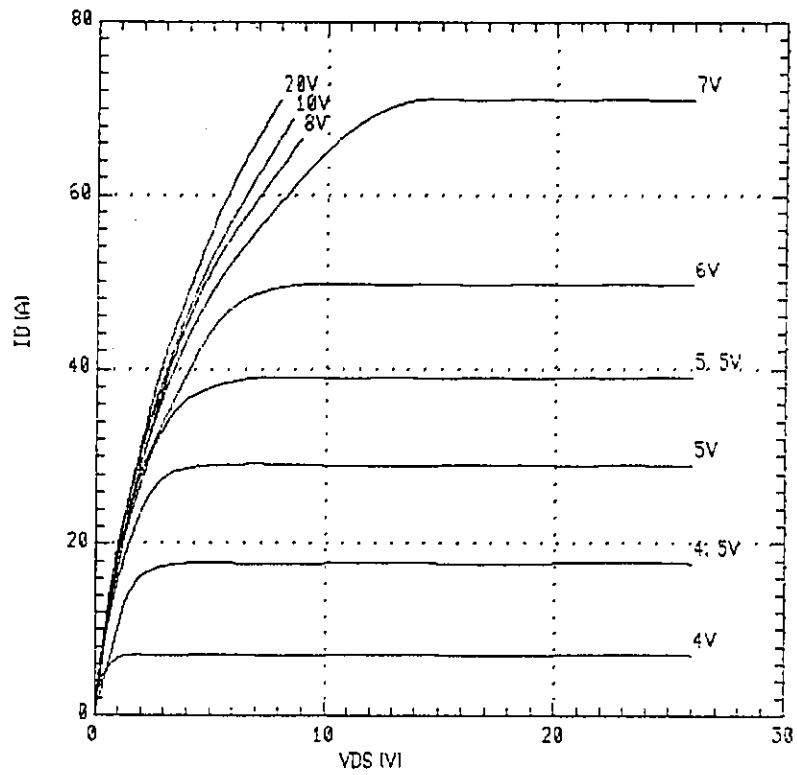
Power Dissipation  
 $PD=f(TC)$



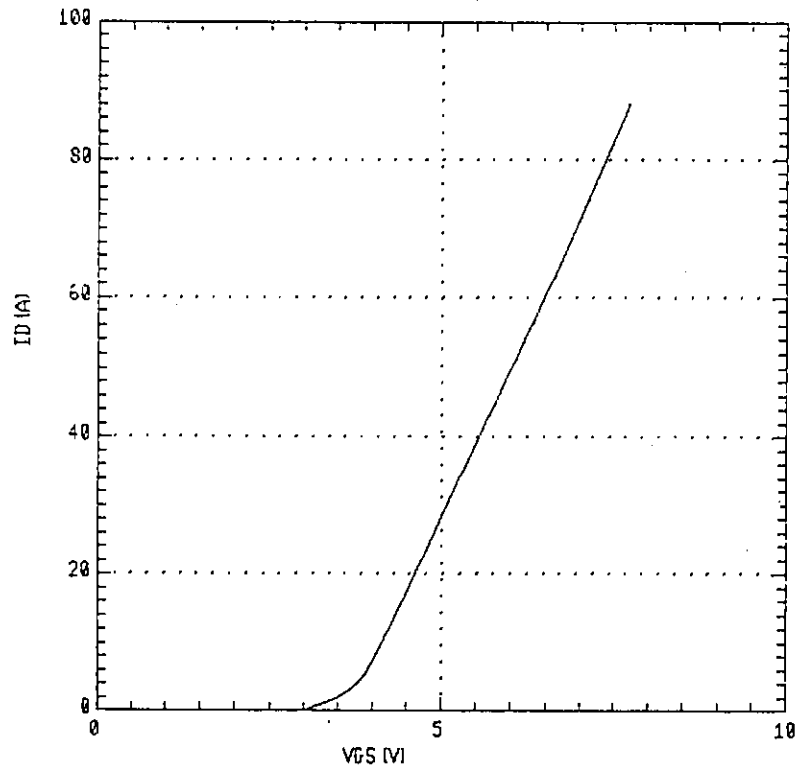
Safe operating area  
 $ID=f(VDS): D=0.01, Tc=25^{\circ}C$



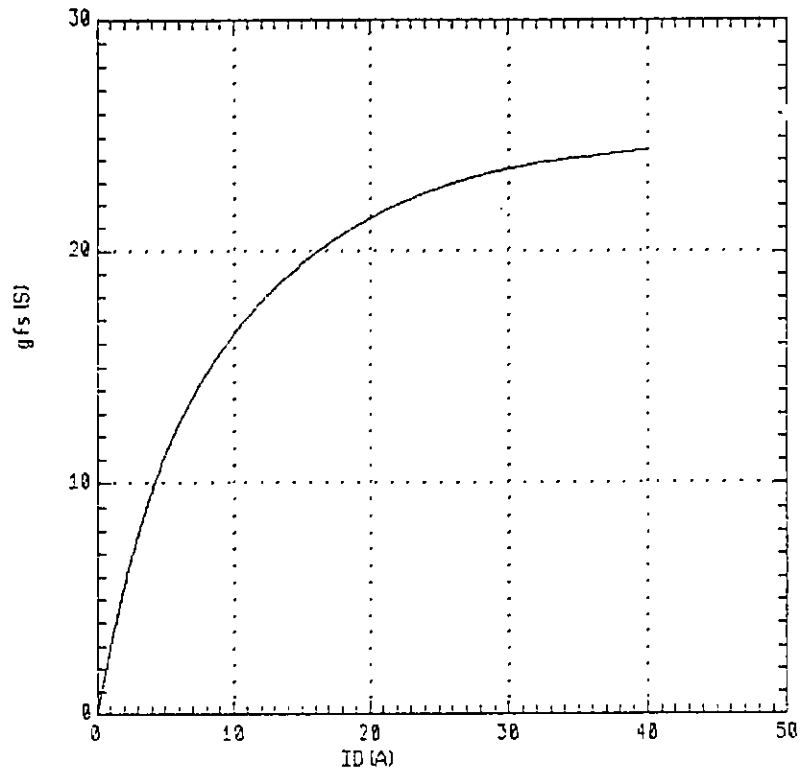
Typical output characteristics  
 $I_D = f(V_{DS})$ :  $80 \mu s$  pulse test,  $T_{ch} = 25^\circ C$



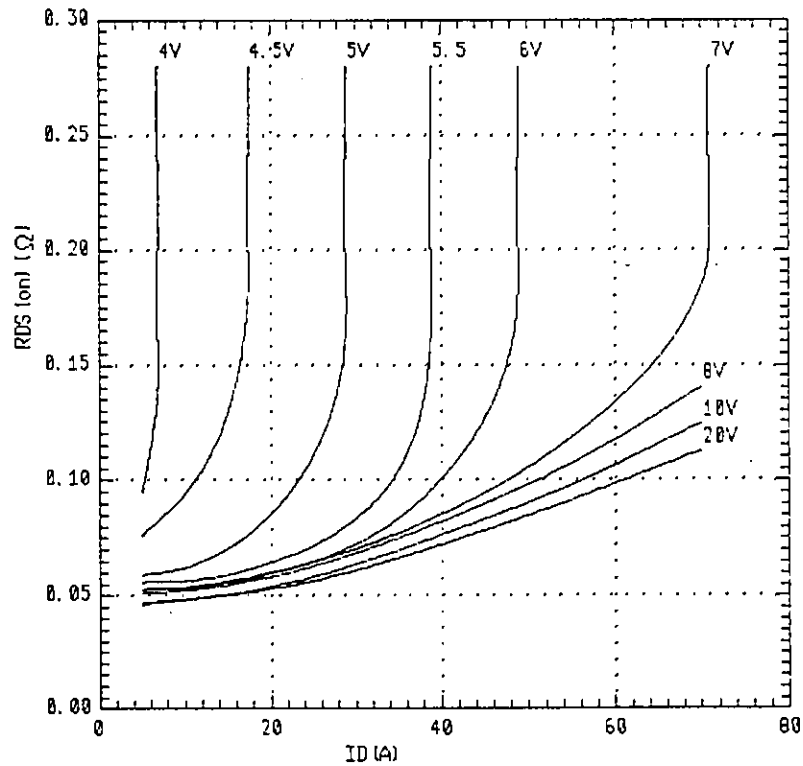
Typical Transfer Characteristic  
 $I_D = f(V_{GS})$ :  $80 \mu s$  pulse test,  $V_{DS} = 25V$ ,  $T_{ch} = 25^\circ C$



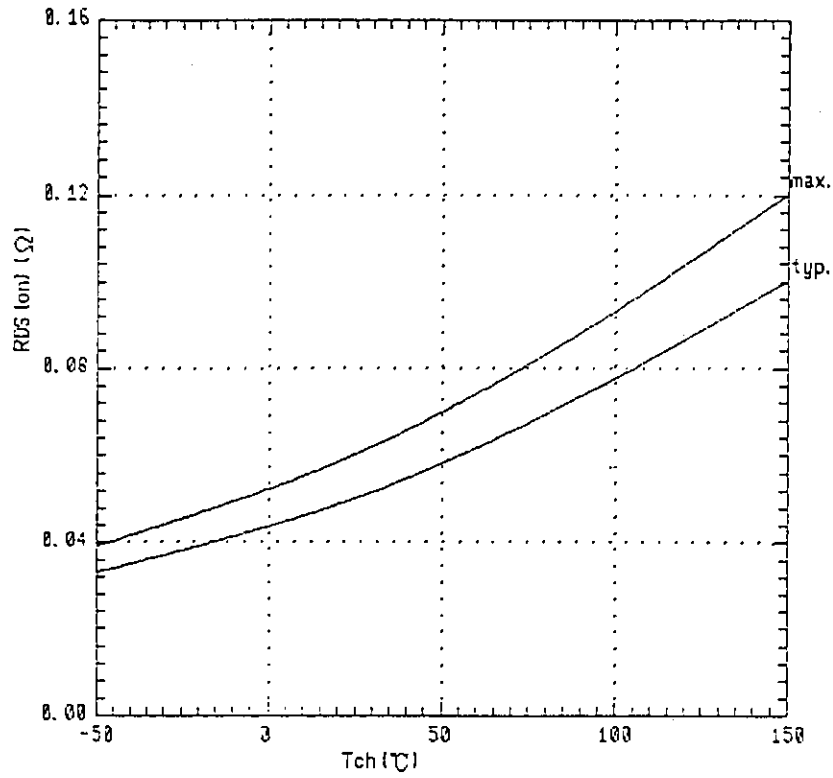
Typical Transconductance  
 $g_{fs}=f(I_D):80\mu s$  pulse test,  $V_{DS}=25V, T_{ch}=25^\circ C$



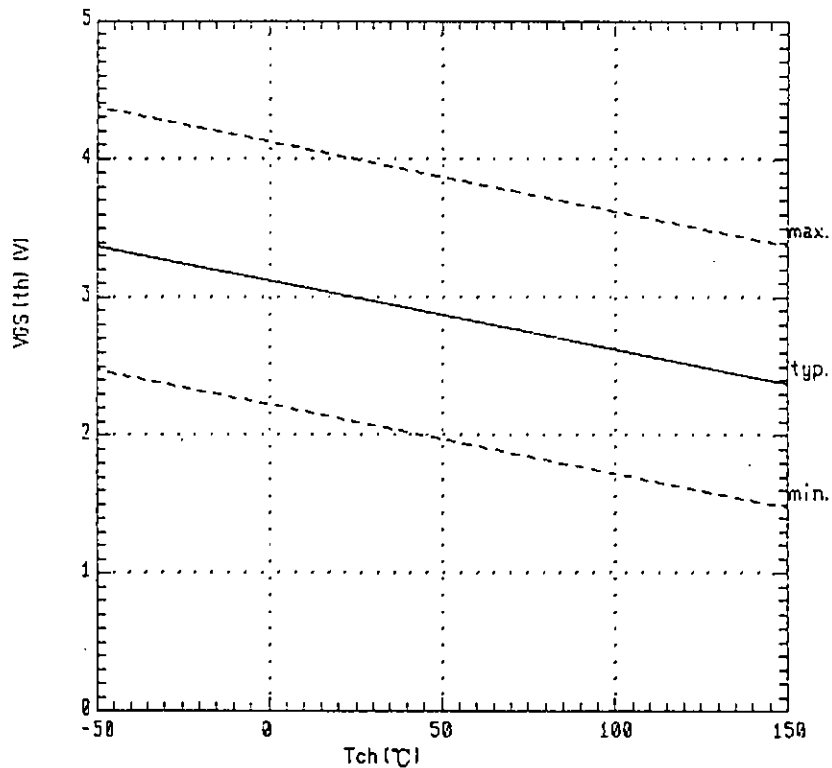
Typical Drain-source on-state resistance  
 $R_{DS(on)}=f(I_D):80\mu s$  pulse test,  $T_{ch}=25^\circ C$



Drain-source on-state resistance  
 $R_{DS(on)} = f(T_{ch}) : I_D = 16A, V_{GS} = 10V$

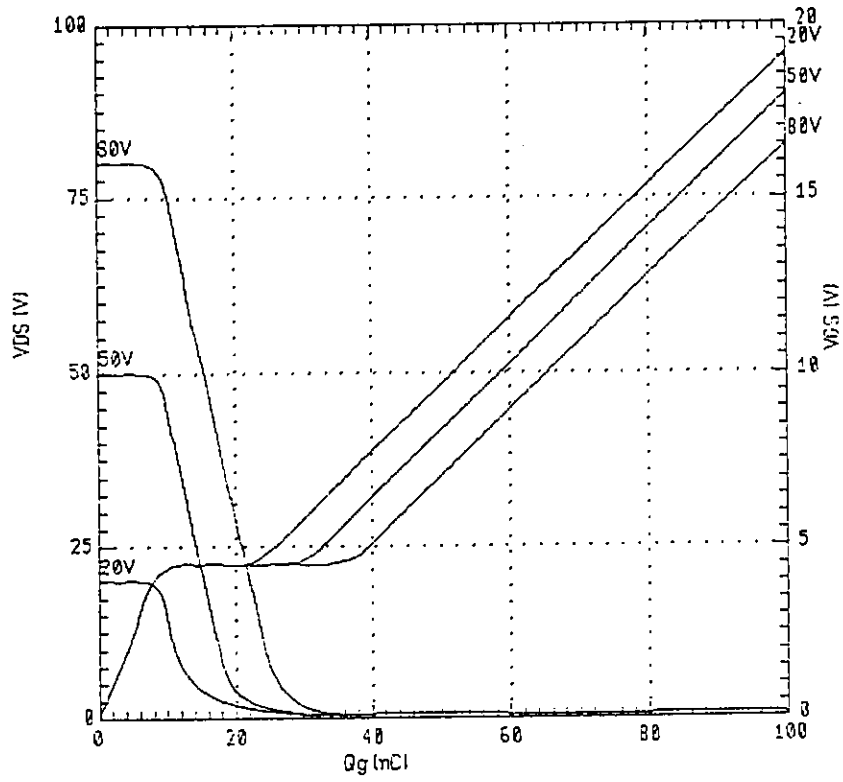


Gate threshold voltage  
 $V_{GS(th)} = f(T_{ch}) : V_{DS} = V_{GS}, I_D = 10mA$

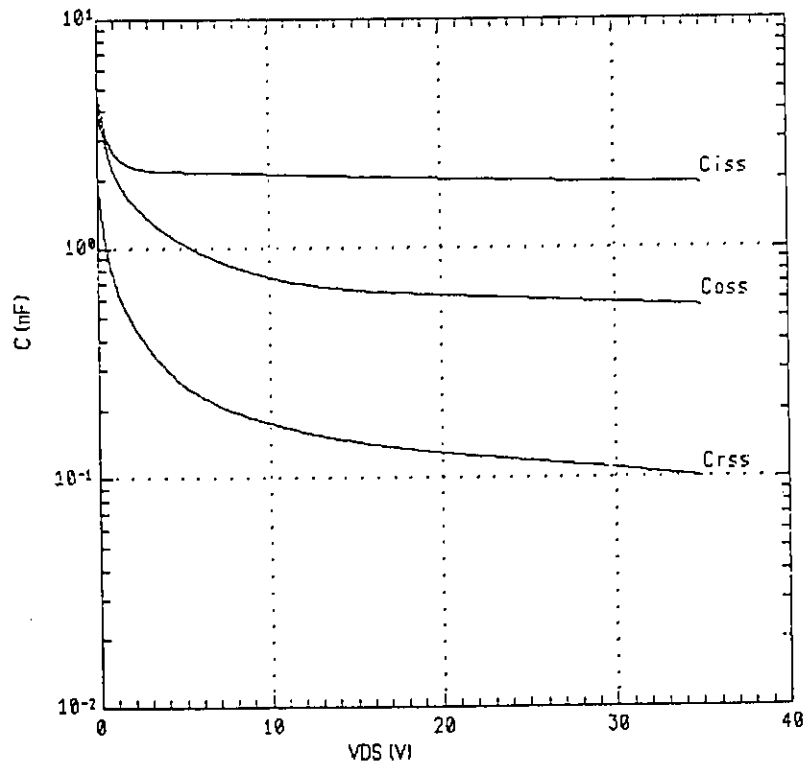




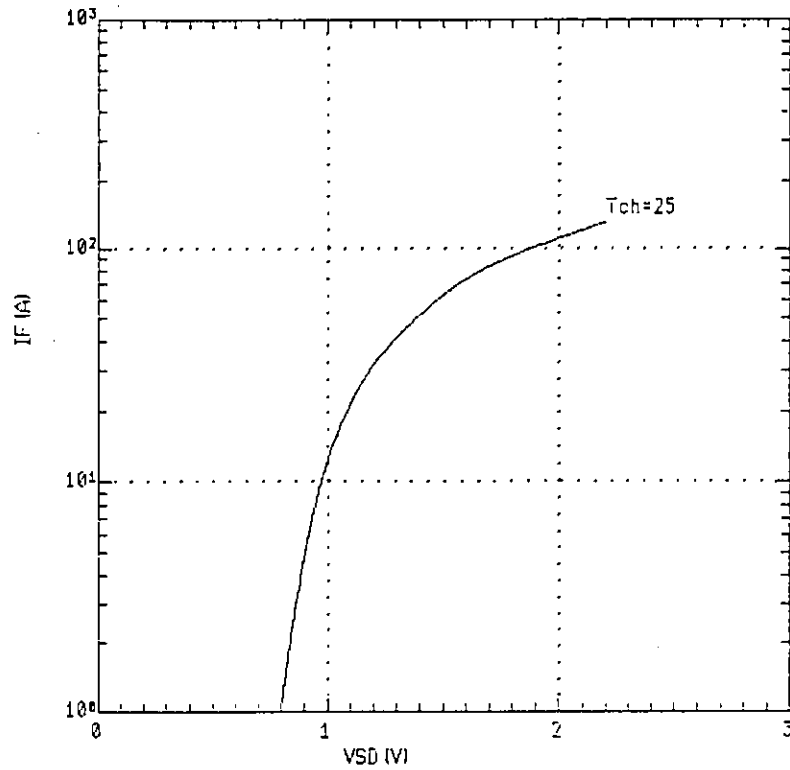
Typical gate charge characteristics  
 $V_{GS} = f(Q_g) : I_D = 32A, T_{ch} = 25^\circ C$



Typical capacitances  
 $C = f(V_{DS}) : V_{GS} = 0V, f = 1MHz$



Forward characteristic of reverse diode  
 $I_F = f(V_{SD}) : 80 \mu s$  pulse test



Transient thermal impedance  $Z_{thch-c} = f(t)$  parameter:  $D = t/T$

