

FA7630CP(E)

Bipolar IC
For Switching Power Supply Control

■ Description

The FA7630CP(E) is dual channel PWM-type switching power supply control IC.

This IC can drive Pch-MOSFET directly and is optimum for large power DC-DC converter.

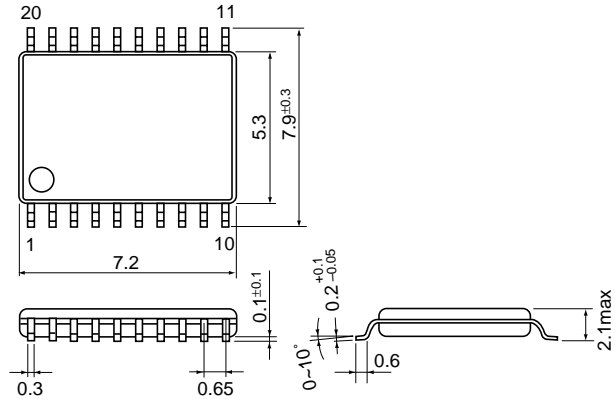
This IC has all the necessary protection functions for a power MOSFET. It is optimum for a portable equipment power supply which uses low-voltage input to output comparably large power.

■ Features

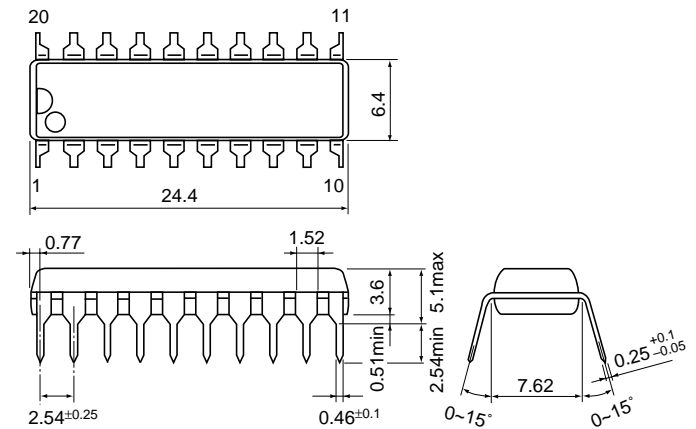
- Dual channel PWM-type switching power supply control
- $\pm 2.6\%$ (V_{REF}) and $\pm 3\%$ (V_B) reference voltage
- Low voltage operation (V_{CC} : 5 to 20V)
- Drive circuit for connecting a Pch-MOSFET
- Overload cutoff circuit with timer latch circuit
- Soft start circuit
- Undervoltage lockout function (UVLO)
- Pulse-by-pulse overcurrent limiting function
- Output ON/OFF control function for each channel
- Master-slave synchronized operation without external components
- Maximum operating frequency is 500kHz

■ Dimensions, mm

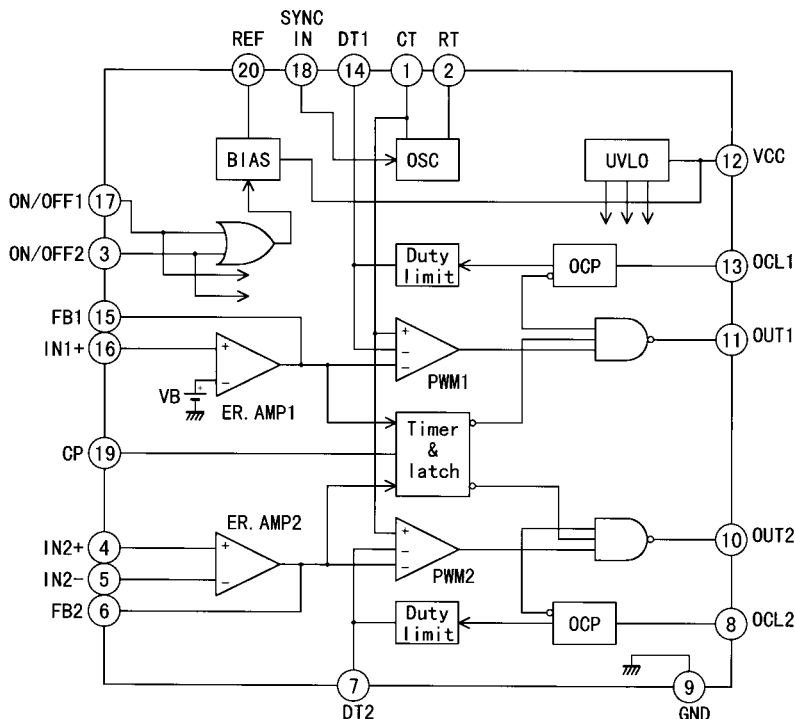
● SSOP-20



● DIP-20



■ Block diagram



Pin No.	Pin symbol	Description
1	CT	Oscillator timing capacitor
2	RT	Oscillator timing resistor
3	ON/OFF2	Output ON/OFF control 2
4	IN2+	Non-inverting input to error amplifier
5	IN2-	Inverting input to error amplifier
6	FB2	Error amplifier output
7	DT2	Dead time adjustment
8	OCL2	Overcurrent limiting circuit 2
9	GND	Ground
10	OUT2	CH.2 output
11	OUT1	CH.1 output
12	VCC	Power supply
13	OCL1	Overcurrent limiting circuit
14	DT1	Dead time adjustment
15	FB1	Error amplifier output
16	IN1+	Non-inverting input to error amplifier
17	ON/OFF1	Output ON/OFF control 1
18	SYNC IN	Synchronizing signal input
19	CP	Timer and latch circuit
20	REF	Reference voltage output

■ Absolute maximum ratings

Item	Symbol	Rating	Unit
Maximum supply voltage	V _{CC}	22	V
Maximum output current	I _{OUT}	±500	mA
Maximum power dissipation	P _d	650	mW
Junction temperature	T _J	125	°C
Operating temperature	T _{opr}	-20 to +85	°C
Storage temperature	T _{stg}	-40 to +150	°C

■ Recommended operating conditions

Item	Symbol	Min.	Max.	Unit
Supply voltage	V _{CC}	5	20	V
Feedback resistance	R _{NF}	100		kΩ
Oscillator timing capacitance	C _T	50	2200	pF
Oscillator timing resistance	R _T	24	100	kΩ
Oscillation frequency	f _{osc}	50	500	kHz

■ Electrical characteristics (T_a=25°C, V_{CC}=6V, C_T=180pF, R_T=47kΩ)

Reference voltage section

Item	Symbol	Test condition	Min.	Typ.	Max.	Unit
Output voltage	V _{REF}	I _{OR} = 1mA	2.450	2.520	2.580	V
Line regulation	L _{INE}	V _{CC} = 5.0 to 20V I _{OR} =1mA		5	20	mV
Load regulation	L _{OAD}	I _{OR} = 0.1 to 1mA		30	50	mV
Output voltage variation due to temperature change	V _{TC1}	T _a = -20 to +25°C	-1		1	%
	V _{TC2}	T _a = +25 to +85°C	-1		1	%

Oscillation and synchronizing circuit section

Item	Symbol	Test condition	Min.	Typ.	Max.	Unit
Oscillation frequency	f _{osc}	C _T =180pF, R _T =47kΩ	74	83	92	kHz
Frequency variation(due to supply voltage change)	f _{dV}	V _{CC} = 3.6 to 20V		1		%
Frequency variation(due to temperature change)	f _{dT}	T _a = -20 to +85°C		5		%
H level of synchronizing signal	V _{SYH}		1.5		1.8	V
L level of synchronizing signal	V _{SYL}		0.8		1.1	V
Cutoff level of synchronizing signal	V _{SD}				0.4	V
Bias current of synchronizing signal	I _{SYNC}	V _{SYNC} =1.6V			5	μA

Error amplifier section (Ch.1)

Item	Symbol	Test condition	Min.	Typ.	Max.	Unit
Reference voltage	V _B		0.769	0.793	0.817	V
Input bias current	I _B			5	100	nA
Open-loop voltage gain	A _{VO}			60		dB
Unity-gain bandwidth	f _T			1		MHz
H state output voltage	V _{OH}	No load	2.10			V
L state output voltage	V _{OL}	No load			300	mV
Output source current	I _{OH}	V _{OH} =1V	40	55	100	μA

Error amplifier section (Ch.2)

Item	Symbol	Test condition	Min.	Typ.	Max.	Unit
Input offset voltage	V _{IO}			2	10	mV
Input bias current	I _B			5	100	nA
Common-mode input voltage	V _{COM}		0		1.0	V
Open-loop voltage gain	A _{VO}		70			dB
Unity-gain bandwidth	f _T			1.0		MHz
H state output voltage	V _{OH}	No load	2.10			V
L state output voltage	V _{OL}	No load			300	mV
Output source current	I _{OH}	V _{OH} =1V	50	65	200	μA

Pulse width modulation circuit section (FB pin)

Item	Symbol	Test condition	Min.	Typ.	Max.	Unit
Input threshold voltage	V _{TH0}	Duty cycle = 0%		1.65		V
Input threshold voltage	V _{TH1}	Duty cycle = 100%		0.95		V

Dead time adjustment circuit section (DT pin)

Item	Symbol	Test condition	Min.	Typ.	Max.	Unit
Input threshold voltage	V _{TH0}	Duty cycle = 0%		1.65		V
Input threshold voltage	V _{TH1}	Duty cycle = 100%		0.95		V
Standby voltage	V _{STB}	DT pin open	1.80			V

Overcurrent limiting circuit section

Item	Symbol	Test condition	Min.	Typ.	Max.	Unit
Input threshold voltage	V _{THOC}		175	200	225	mV
Hysteresis voltage	V _{HYOC}			40		mV
Input bias current	I _{OC}			45	80	μA
Delay in OCL	t _{DOC}	Overdriving : 50mV		120		ns

Timer and latch circuit section

Item	Symbol	Test condition	Min.	Typ.	Max.	Unit
Latch-mode threshold voltage	V _{THCP}		1.00	1.25	1.50	V
Input bias current	I _{INCP}	V _{CP} =1.5V, V _{FB} =0.3V			1	μA
CP pin voltage / LOW	V _{SATC}	I _{CP} =20μA, V _{FB} =1.0V			300	mV

Output ON/OFF control section

Item	Symbol	Test condition	Min.	Typ.	Max.	Unit
OFF-to-ON threshold voltage	V _{THON}				3.0	V
ON-to-OFF threshold voltage	V _{THOF}		0.60			V
Input bias current	I _{IN}	V _{IN} =3V		230	350	μA

Undervoltage lock-out circuit section

Item	Symbol	Test condition	Min.	Typ.	Max.	Unit
OFF-to-ON threshold voltage	V _{CCON}		3.92	4.20	4.48	V
ON-to-OFF threshold voltage	V _{CCOF}			3.50		V
Voltage hysteresis	V _{HYS}			0.70		V

Output section

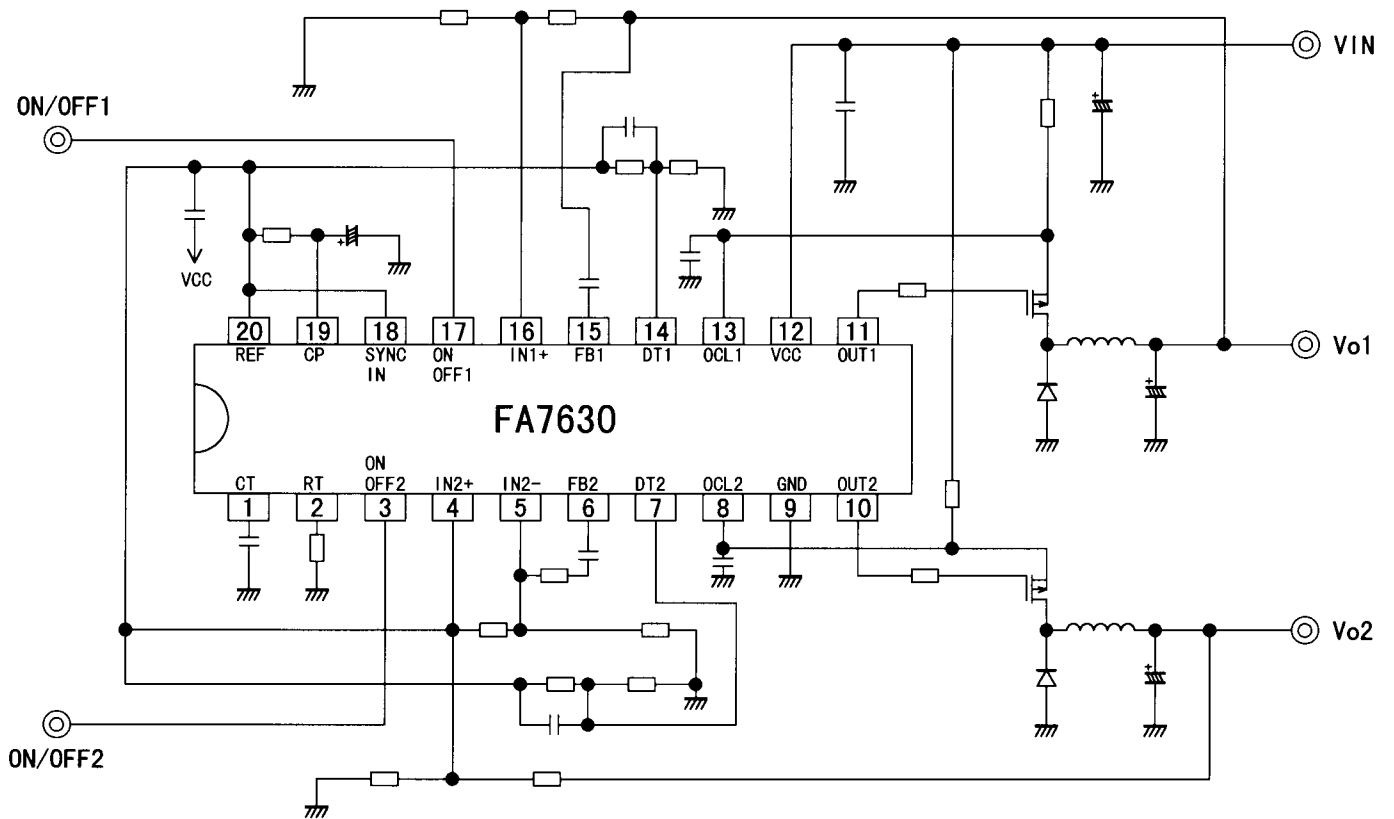
Item	Symbol	Test condition	Min.	Typ.	Max.	Unit
Saturation voltage(H level)	V _{SAT+}	I _O = -50mA		1.60	2.00	V
Saturation voltage(L level)	V _{SAT-}	I _O = 50mA		1.40	1.80	V

Overall device

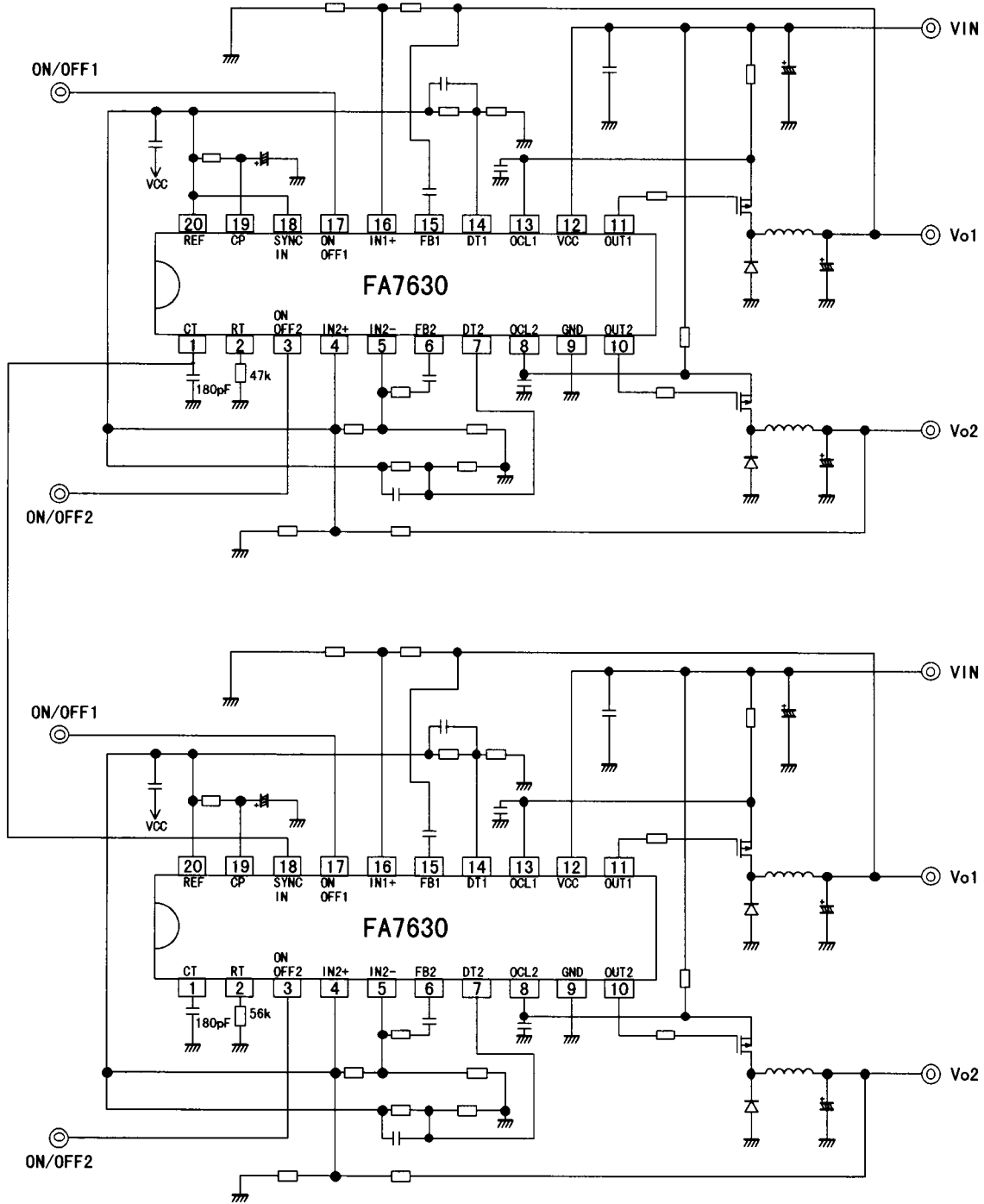
Item	Symbol	Test condition	Min.	Typ.	Max.	Unit
Standby supply current	I _{CCST}	Out pin open		0.1	10	μA
Operating-state supply current	I _{CC1}	Single channel operation Duty cycle =50%		2.20	3.20	mA
	I _{CC2}	Dual channel operation Duty cycle =50%		3.00	4.20	mA

■ Application circuit

● Individual application circuit



• Master-slave application circuit



Parts tolerances characteristics are not defined in the circuit design sample shown above. When designing an actual circuit for a product, you must determine parts tolerances and characteristics for safe and economical operation.